

Name and Student ID: _____ Lab Section: _____

Date: _____

PRELAB:

Q1. Fill in the Truth Table below for an AND gate:

A	B	C
0	0	
0	1	
1	0	
1	1	

Q2. What does the .bdf file extension stand for?

Q3. What is the name of the FPGA on the DE2 board?

TA Initials: _____

LAB:

2.0 Fill in the Truth Table for *lab1step1*:

A	B	C
0	0	
0	1	
1	0	
1	1	

Logic Expression: _____

TA Initials: _____

4.0 Fill in the Truth Table for *lab1step2*:

W	X	Y	Z
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

Logic Expression: _____

TA Initials: _____

4.0 Fill in the Truth Table for *lab1step3*:

A	B	C	F

Logic Expression: _____

TA Initials: _____