

CprE 281: Class Schedule (Spring 2014)

Week	Day/Date	Topic	Readings	Homework	Lab
1	Monday 1/13	Introduction	1.1, 1.2, 1.3, 1.4		No Lab
	Wednesday 1/15	Binary Numbers	1.5, 1.6		
	Friday 1/17	Truth Tables & Logic Gates	2.1, 2.2, 2.3, 2.4		
2	Monday 1/20	NO CLASS: University Holiday			Lab 1
	Wednesday 1/22	Boolean Algebra	2.5	HW 1 due	
	Friday 1/24	AND, OR, NOT	2.6		
3	Monday 1/27	NAND, NOR	2.7	HW 2 due	Lab 2
	Wednesday 1/29	Design Examples	2.8		
	Friday 1/31	Intro to Verilog (part 1)	2.9, 2.10		
4	Monday 2/3	Intro to Verilog (part 2)	2.9, 2.10	HW 3 due	Lab 3
	Wednesday 2/5	Karnaugh Maps	2.11		
	Friday 2/7	Minimization	2.12, 2.13		
5	Monday 2/10	Functions and Circuits	2.14, 2.15, 2.16	HW 4 due	Lab 4
	Wednesday 2/12	Examples	2.17		
	Friday 2/14	Addition of Unsigned Numbers	3.1, 3.2		
6	Monday 2/17	Signed Numbers	3.3	HW 5 due	Lab 5
	Wednesday 2/19	Fast Adders	3.4, 3.5		
	Friday 2/21	Midterm Review Session			
7	Monday 2/24	Midterm #1			Mini Project
	Wednesday 2/26	Multiplication	3.6		
	Friday 2/28	Floating Point Numbers	3.7		
8	Monday 3/3	Multiplexers	4.1	HW 6 due	Lab 6
	Wednesday 3/5	Decoders & Encoders	4.2, 4.3		
	Friday 3/7	Code Converters	4.4, 4.5		
	Friday 3/7	* Midterm Grade Reports Due *			
9	Monday 3/10	Latches	5.1, 5.2, 5.3	HW 7 due	Lab 7
	Wednesday 3/12	D Flip-Flops	5.4		
	Friday 3/14	T Flip-Flops & JK Flip-Flops	5.5, 5.6, 5.7		

Week	Day/Date	Topic	Readings	Homework	Lab
10	Monday 3/17	NO CLASS: Spring Break			No Lab
	Wednesday 3/19	NO CLASS: Spring Break			
	Friday 3/21	NO CLASS: Spring Break			
11	Monday 3/24	Registers	5.8	HW 8 due	Lab 8
	Wednesday 3/26	Counters	5.9, 5.10		
	Friday 3/28	Midterm Review Session			
	Friday 3/28	***DROP DEADLINE***			
12	Monday 3/31	Midterm # 2			Lab 9
	Wednesday 4/2	Examples with Counters	5.11, 5.14, 5.17		
	Friday 4/4	Basic Design Steps	6.1		
13	Monday 4/7	State-Assignment Problem	6.2	HW 9 due	Lab 10
	Wednesday 4/9	Moore & Mealy Machines	6.3, 6.4		
	Friday 4/11	Serial Adder	6.5		
14	Monday 4/14	State Minimization	6.6	HW 10 due	Lab 11
	Wednesday 4/16	Designing a Counter	6.7		
	Friday 4/18	Arbiter Circuit	6.8		
15	Monday 4/21	Analysis of Syn. Seq. Circuits	6.9	HW 11 Due	Work on
	Wednesday 4/23	ASM charts	6.10, 6.11		Final
	Friday 4/25	Examples	6.12, 6.13		Project
16	Monday 4/28	TBD		HW 12 due	Final
	Wednesday 4/30	TBD			Project
	Friday 5/2	Final Review Session			Demo
17	Finals Week 5/??	FINAL EXAM @ (time TBD)	everything		No Lab