T1. Review HW02 and solve any problems that students point out they had difficulty with.

- T2. Answer any general questions about HW03 and Lab 02 that students ask.
- T3. Solve the following problems in order until time runs out.
  - 1. Solve problem P8 of HW02.
  - Use algebraic manipulation to find the minimum product-of-sums expression for the function f=(x1+x2+x3).(x1+x2'+x3).(x1'+x2'+x3).(x1+x2+x3')
  - Use Venn diagram to prove the two forms of DeMorgan's theorem:
     (a) (X.Y)' = X' + Y' and (b) (X+Y)' = X'.Y'
  - 4. Reduce the expressions below using axioms, theorems, and properties of Boolean algebra. Then, draw the circuit diagram for each of the reduced expressions, and find the cost of each circuit as the sum of the number of gates and the number of gate inputs.
    (a) X.Y.(X'.Y+X'.Y')

(b) X . Y . (Z + Y Z') + Z'

- 5. Reduce the following expression to the simplest form. (a)  $\Sigma$ m(1,2,3,4,5,6,7)
  - (b) ∏M(0,1,2,3,4,5,6).