

What to submit?

- A top-level diagram of your design, showing inputs, outputs, components, and the sizes of data signals and the meanings assigned to all of these.
- A list of all block elements that you had to design for this project
- For all/most components, also provide a written description (e.g., the finite state machine, the register file, adder, etc.). This description should include things like state tables, corresponding state assigned tables, truth tables, K-maps, expressions, code, and visual images of any blocks created for this project.
- Description and purpose of any gates used outside of created blocks
- Include several test cases that a user could follow to see the functionality of the circuit
- It is OK for some of these to submit hand written notes/descriptions. In other words, you don't have to type everything.
- All submitted materials should be neatly organized in a professional manner so that your design is easy to follow and understand. Staple together all of your pages or use a binder clip or put all of them a folder. Make sure to write your name, student ID, and lab section letter on the first page.
- **To get a grade you must demonstrate your circuit to your TA.** Go through at least two of your test cases to demonstrate its functionality. The TA may choose to try other test cases as well.
- Finally, e-mail a ZIP file of the folder that contains your final project to your TA.

When is the deadline?

All materials described above must be submitted by the end of your last lab, during dead week. For example, if your regular lab is on Tuesday from 2-5pm, then your final project is due by 5pm that day.