

## CprE 281: Class Schedule (Fall 2021)

Week	Day/Date	Topic	Readings	Homework	Lab
<b>1</b>	Monday 8/23	Introduction	1.1, 1.2, 1.3, 1.4		No Lab
	Wednesday 8/25	Binary Numbers	1.5, 1.6		
	Friday 8/27	Truth Tables & Logic Gates	2.1, 2.2, 2.3, 2.4		
<b>2</b>	Monday 8/30	Boolean Algebra	2.5	HW 1 due	Lab 1
	Wednesday 9/1	AND, OR, NOT	2.6		
	Friday 9/3	NAND, NOR	2.7		
<b>3</b>	Monday 9/6	NO CLASS: University Holiday			Lab 2
	Wednesday 9/8	Design Examples	2.8	HW 2 due	
	Friday 9/10	Intro to Verilog	2.9, 2.10		
<b>4</b>	Monday 9/13	Karnaugh Maps	2.11	HW 3 due	Lab 3
	Wednesday 9/15	Minimization	2.12, 2.13		
	Friday 9/17	Functions and Circuits	2.14, 2.15, 2.16		
<b>5</b>	Monday 9/20	Examples	2.17	HW 4 due	Lab 4
	Wednesday 9/22	Midterm Review Session			
	Friday 9/24	Midterm #1			
<b>6</b>	Monday 9/27	Addition of Unsigned Numbers	3.1, 3.2	HW 5 due	Lab 5
	Wednesday 9/29	Signed Numbers	3.3		
	Friday 10/1	Fast Adders	3.4, 3.5		
<b>7</b>	Monday 10/4	Multiplication	3.6		Mini Project
	Wednesday 10/6	Floating Point Numbers	3.7		
	Friday 10/8	Multiplexers	4.1		
<b>8</b>	Monday 10/11	Decoders & Encoders	4.2, 4.3	HW 6 due	Lab 6
	Wednesday 10/13	Code Converters	4.4, 4.5		
	Friday 10/15	Latches	5.1, 5.2, 5.3		
	Friday 10/15	* Midterm Grade Reports Due *			
<b>9</b>	Monday 10/18	D Flip-Flops	5.4	HW 7 due	Lab 7
	Wednesday 10/20	T Flip-Flops & JK Flip-Flops	5.5, 5.6, 5.7		
	Friday 10/22	Registers & Register Files	5.8		

Week	Day/Date	Topic	Readings	Homework	Lab
<b>10</b>	Monday 10/25	Counters	5.9, 5.10	HW 8 due	Lab 8
	Wednesday 10/27	Midterm Review Session			
	Friday 10/29	Midterm # 2			
	Friday 10/29	***DROP DEADLINE***			
<b>11</b>	Monday 11/1	Examples with Counters	5.11, 5.14, 5.17	HW 9 due	Lab 9
	Wednesday 11/3	Basic Design Steps	6.1		
	Friday 11/5	State-Assignment Problem	6.2		
<b>12</b>	Monday 11/8	Moore & Mealy Machines	6.3, 6.4	HW 10 due	Lab 10
	Wednesday 11/10	Serial Adder & Arbiter Circuit	6.5, 6.8		
	Friday 11/12	State Minimization	6.6		
	Friday 11/12	PROPOSE FINAL PROJECT			
<b>13</b>	Monday 11/15	Designing a Counter	6.7	HW 11 due	Lab 11
	Wednesday 11/17	Analysis of Syn. Seq. Circuits	6.9		
	Friday 11/19	ASM charts & Examples	6.10 – 6.13		
<b>14</b>	Monday 11/22	NO CLASS: Thanksgiving Break			No Lab
	Wednesday 11/24	NO CLASS: Thanksgiving Break			
	Friday 11/26	NO CLASS: Thanksgiving Break			
<b>15</b>	Monday 11/29	Register Machines		HW 12 due	Work on Final Project
	Wednesday 12/1	The i281 CPU			
	Friday 12/3	i281 Assembly Language			
<b>16</b>	Monday 12/6	Arithmetic Logic Unit & PC			Final Project Demo
	Wednesday 12/8	Software-Hardware Interface			
	Friday 12/10	Final Review Session			
<b>17</b>	Finals Week 12/15	FINAL EXAM @ 7:30–9:30am	everything		No Lab