**DE2-115 Board I/O Pin Assignments: Switches, LEDs, and 7-Segment Displays**

**Table 1: Daughter Board Pin assignments**

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| --- | --- | --- |
| **Signal Name** | **FPGA Pin No.** | **Description** |
| **SW3\_DB** | **PIN\_AB22** | **Rocker Switch[3]** |
| **SW2\_DB** | **PIN\_AB21** | **Rocker Switch[2]** |
| **SW1\_DB** | **PIN\_AC21** | **Rocker Switch[1]** |
| **SW0\_DB** | **PIN\_AD21** | **Rocker Switch[0]** |
| **KEY1\_DB** | **PIN\_AC19** | **Push-button[1]** |
| **KEY0\_DB** | **PIN\_AD19** | **Push-button[0]** |
| **LEDR3\_DB** | **PIN\_AF24** | **LED Red[3]** |
| **LEDR2\_DB** | **PIN\_AF25** | **LED Red[2]** |
| **LEDR1\_DB** | **PIN\_AE22** | **LED Red[1]** |
| **LEDR0\_DB** | **PIN\_AF22** | **LED Red[0]** |
| **LEDG1\_DB** | **PIN\_AG25** | **LED Green[1]** |
| **LEDG0\_DB** | **PIN\_AH25** | **LED Green[0]** |

**Table 2: Pin assignments for slide switches**

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**Table 3: Pin assignments for pushbutton (debounced) switches**

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**Table 4: Pin assignments for LEDs**

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**Table 5: Pin assignments for 7-segment displays**

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| --- | --- | --- | --- |
| **Signal Name** | **FPGA Pin No.** | **Description** | **I/O Standard** |
| **HEX0[0]** | **PIN\_G18** | **Seven Segment Digit 0[0]** | **2.5V** |
| **HEX0[1]** | **PIN\_F22** | **Seven Segment Digit 0[1]** | **2.5V** |
| **HEX0[2]** | **PIN\_E17** | **Seven Segment Digit 0[2]** | **2.5V** |
| **HEX0[3]** | **PIN\_L26** | **Seven Segment Digit 0[3]** | **Depending on JP7** |
| **HEX0[4]** | **PIN\_L25** | **Seven Segment Digit 0[4]** | **Depending on JP7** |
| **HEX0[5]** | **PIN\_J22** | **Seven Segment Digit 0[5]** | **Depending on JP7** |
| **HEX0[6]** | **PIN\_H22** | **Seven Segment Digit 0[6]** | **Depending on JP7** |
| **HEX1[0]** | **PIN\_M24** | **Seven Segment Digit 1[0]** | **Depending on JP7** |
| **HEX1[1]** | **PIN\_Y22** | **Seven Segment Digit 1[1]** | **Depending on JP7** |
| **HEX1[2]** | **PIN\_W21** | **Seven Segment Digit 1[2]** | **Depending on JP7** |
| **HEX1[3]** | **PIN\_W22** | **Seven Segment Digit 1[3]** | **Depending on JP7** |
| **HEX1[4]** | **PIN\_W25** | **Seven Segment Digit 1[4]** | **Depending on JP7** |
| **HEX1[5]** | **PIN\_U23** | **Seven Segment Digit 1[5]** | **Depending on JP7** |
| **HEX1[6]** | **PIN\_U24** | **Seven Segment Digit 1[6]** | **Depending on JP7** |
| **HEX2[0]** | **PIN\_AA25** | **Seven Segment Digit 2[0]** | **Depending on JP7** |
| **HEX2[1]** | **PIN\_AA26** | **Seven Segment Digit 2[1]** | **Depending on JP7** |
| **HEX2[2]** | **PIN\_Y25** | **Seven Segment Digit 2[2]** | **Depending on JP7** |
| **HEX2[3]** | **PIN\_W26** | **Seven Segment Digit 2[3]** | **Depending on JP7** |
| **HEX2[4]** | **PIN\_Y26** | **Seven Segment Digit 2[4]** | **Depending on JP7** |
| **HEX2[5]** | **PIN\_W27** | **Seven Segment Digit 2[5]** | **Depending on JP7** |
| **HEX2[6]** | **PIN\_W28** | **Seven Segment Digit 2[6]** | **Depending on JP7** |

|  |  |  |  |
| --- | --- | --- | --- |
| **Signal Name** | **FPGA Pin No.** | **Description** | **I/O Standard** |
| **HEX3[0]** | **PIN\_V21** | **Seven Segment Digit 3[0]** | **Depending on JP7** |
| **HEX3[1]** | **PIN\_U21** | **Seven Segment Digit 3[1]** | **Depending on JP7** |
| **HEX3[2]** | **PIN\_AB20** | **Seven Segment Digit 3[2]** | **Depending on JP7** |
| **HEX3[3]** | **PIN\_AA21** | **Seven Segment Digit 3[3]** | **Depending on JP7** |
| **HEX3[4]** | **PIN\_AD24** | **Seven Segment Digit 3[4]** | **Depending on JP7** |
| **HEX3[5]** | **PIN\_AF23** | **Seven Segment Digit 3[5]** | **Depending on JP7** |
| **HEX3[6]** | **PIN\_Y19** | **Seven Segment Digit 3[6]** | **Depending on JP7** |
| **HEX4[0]** | **PIN\_AB19** | **Seven Segment Digit 4[0]** | **Depending on JP7** |
| **HEX4[1]** | **PIN\_AA19** | **Seven Segment Digit 4[1]** | **Depending on JP7** |
| **HEX4[2]** | **PIN\_AG21** | **Seven Segment Digit 4[2]** | **Depending on JP7** |
| **HEX4[3]** | **PIN\_AH21** | **Seven Segment Digit 4[3]** | **Depending on JP7** |
| **HEX4[4]** | **PIN\_AE19** | **Seven Segment Digit 4[4]** | **Depending on JP7** |
| **HEX4[5]** | **PIN\_AF19** | **Seven Segment Digit 4[5]** | **Depending on JP7** |
| **HEX4[6]** | **PIN\_AE18** | **Seven Segment Digit 4[6]** | **Depending on JP7** |
| **HEX5[0]** | **PIN\_AD18** | **Seven Segment Digit 5[0]** | **Depending on JP7** |
| **HEX5[1]** | **PIN\_AC18** | **Seven Segment Digit 5[1]** | **Depending on JP7** |
| **HEX5[2]** | **PIN\_AB18** | **Seven Segment Digit 5[2]** | **Depending on JP7** |
| **HEX5[3]** | **PIN\_AH19** | **Seven Segment Digit 5[3]** | **Depending on JP7** |
| **HEX5[4]** | **PIN\_AG19** | **Seven Segment Digit 5[4]** | **Depending on JP7** |
| **HEX5[5]** | **PIN\_AF18** | **Seven Segment Digit 5[5]** | **Depending on JP7** |
| **HEX5[6]** | **PIN\_AH18** | **Seven Segment Digit 5[6]** | **Depending on JP7** |
| **HEX6[0]** | **PIN\_AA17** | **Seven Segment Digit 6[0]** | **Depending on JP7** |
| **HEX6[1]** | **PIN\_AB16** | **Seven Segment Digit 6[1]** | **Depending on JP7** |
| **HEX6[2]** | **PIN\_AA16** | **Seven Segment Digit 6[2]** | **Depending on JP7** |
| **HEX6[3]** | **PIN\_AB17** | **Seven Segment Digit 6[3]** | **Depending on JP7** |
| **HEX6[4]** | **PIN\_AB15** | **Seven Segment Digit 6[4]** | **Depending on JP7** |
| **HEX6[5]** | **PIN\_AA15** | **Seven Segment Digit 6[5]** | **Depending on JP7** |
| **HEX6[6]** | **PIN\_AC17** | **Seven Segment Digit 6[6]** | **Depending on JP7** |
| **HEX7[0]** | **PIN\_AD17** | **Seven Segment Digit 7[0]** | **Depending on JP7** |
| **HEX7[1]** | **PIN\_AE17** | **Seven Segment Digit 7[1]** | **Depending on JP7** |
| **HEX7[2]** | **PIN\_AG17** | **Seven Segment Digit 7[2]** | **Depending on JP7** |
| **HEX7[3]** | **PIN\_AH17** | **Seven Segment Digit 7[3]** | **Depending on JP7** |
| **HEX7[4]** | **PIN\_AF17** | **Seven Segment Digit 7[4]** | **Depending on JP7** |
| **HEX7[5]** | **PIN\_AG18** | **Seven Segment Digit 7[5]** | **Depending on JP7** |
| **HEX7[6]** | **PIN\_AA14** | **Seven Segment Digit 7[6]** | **3.3V** |

**Table 6: Pin assignments for clock inputs**

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