

CprE 281: Digital Logic

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http://www.ece.iastate.edu/~alexs/classes/

Algorithmic State Machine (ASM) Charts

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Homework 12 is out

It is due on Wednesday November 30 @ 10pm

- Final Project Proposals
 - Did you hear back from your TAs?
 - Did you make the changes that they recommended?

Extra credit lab is due on December 2 @ 10 pm

- Final Project: due during your last lab (dead week).
 - The grading rubric is posted on the class web page.
 - presentation of your work to your TAs
 - final report pdf (upload on canvas)
 - zip file with your design (upload on canvas)
 - If you work in a team: each student needs to present separately to their lab TAs.

The FINAL exam is scheduled for

Wednesday Dec 14 @ 2:15 - 4:15 PM

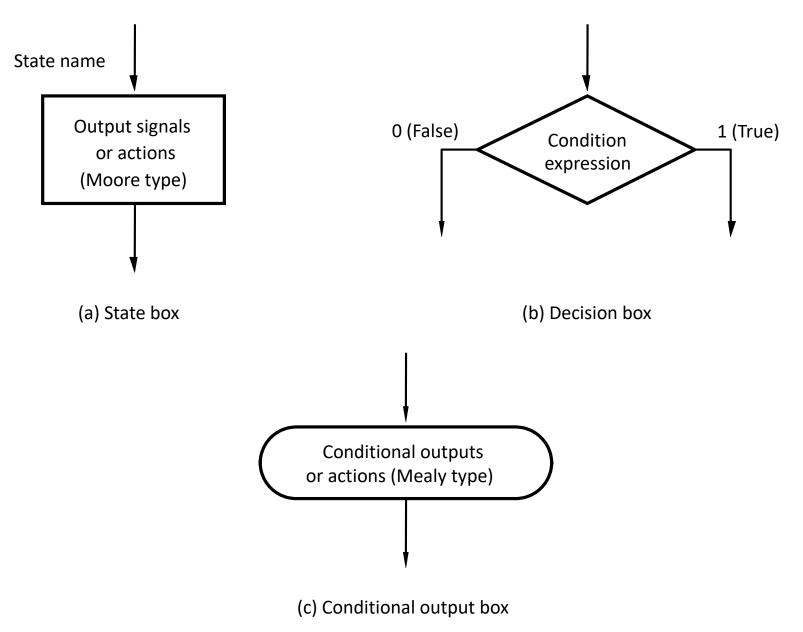
Reading Material for Next Lecture

 "The Seven Secrets of Computer Power Revealed" by Daniel Dennett.

 This is Chapter 24 in his book "Intuition Pumps and Other Tools for Thinking", 2013

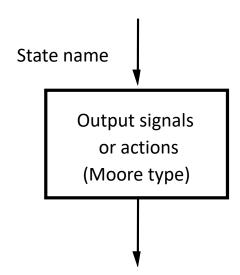


Elements used in ASM charts

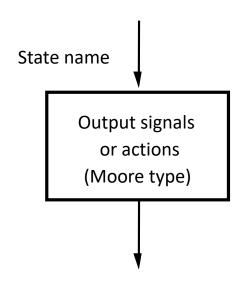


[Figure 6.81 from the textbook]

State Box

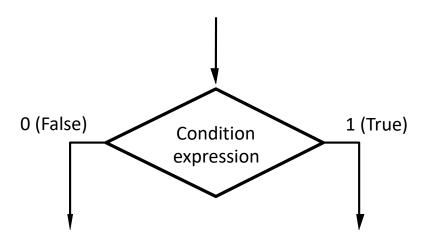


State Box

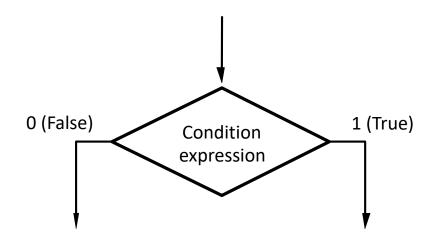


- Indicated with a rectangle
- Equivalent to a node in the State diagram
- The name of the state is written outside the box
- Moore-type outputs are written inside the box
- Only the output that must be set to 1 is written (by default, if an output is not listed it is set to 0)

Decision Box

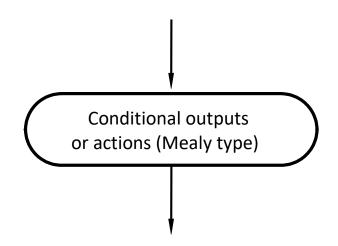


Decision Box



- Indicated with a diamond shape
- Used for a condition expression that must be tested
- The exit path is chosen based on the outcome of the test
- The condition is on one or more inputs to the FSM
- Shortcut notation: w means "is w equal to 1?"

Conditional Output Box



- Indicated with an oval shape
- Used for a Mealy-type output signals
- The outputs depend on the state variables and inputs
- The condition that determines when such outputs are generated is placed in a separate decision box

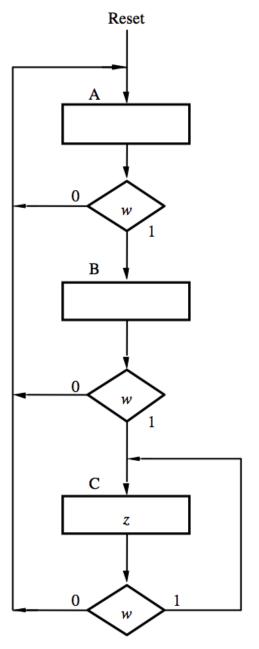
Some Examples

Moore FSM

Reset w = 1A/z = 0B/z = 0w = 0w = 0w = 1w = 0C/z = 1w = 1

[Figure 6.3 from the textbook]

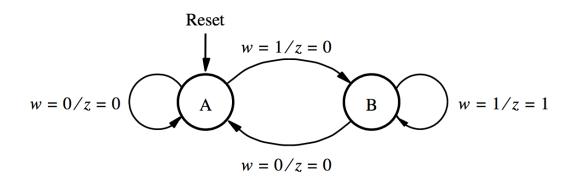
ASM chart

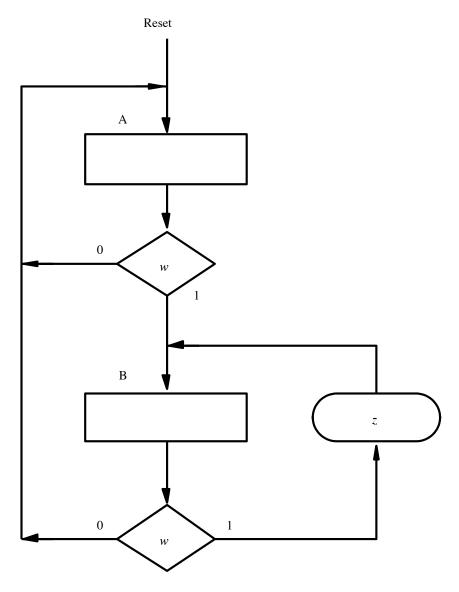


[Figure 6.82 from the textbook]

Mealy FSM

ASM chart



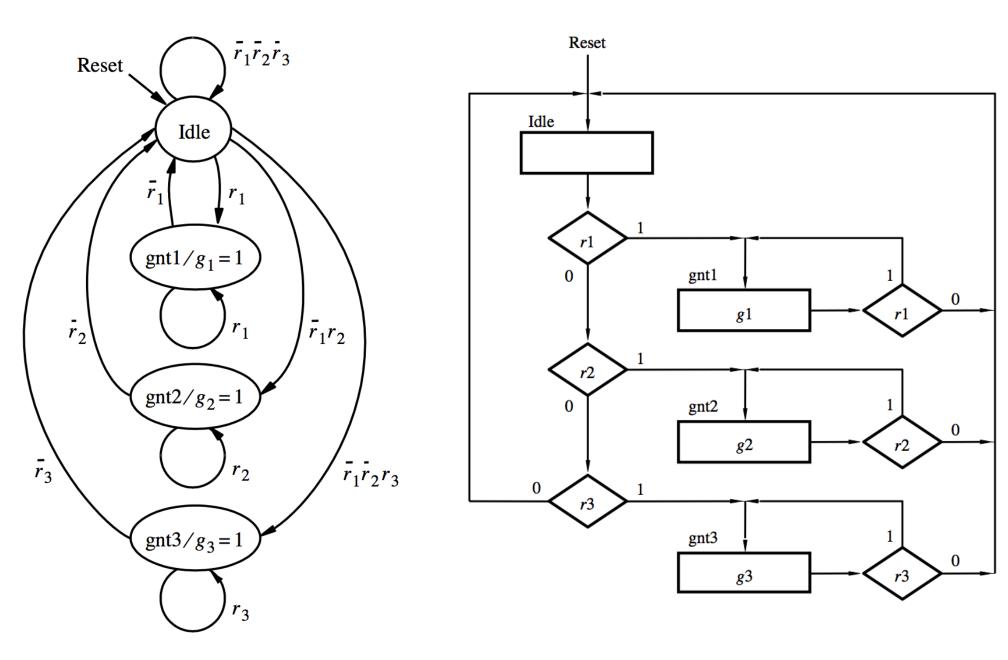


[Figure 6.23 from the textbook]

[Figure 6.83 from the textbook]

FSM

ASM chart



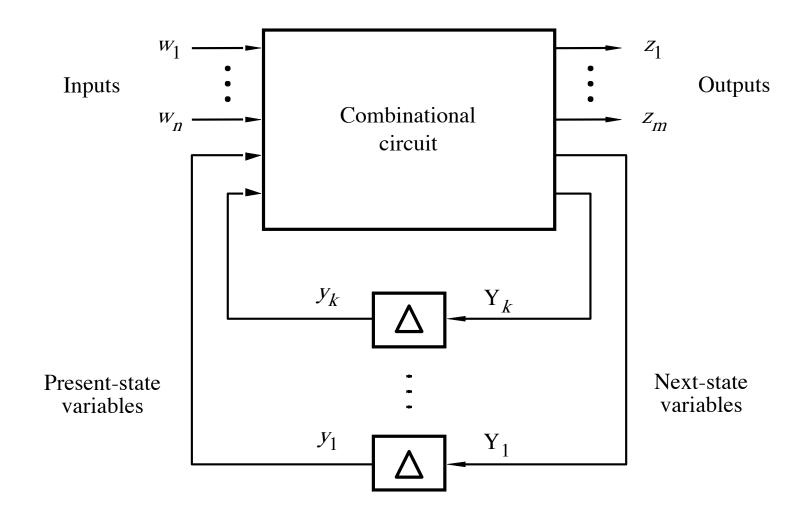
[Figure 6.73 from the textbook]

[Figure 6.84 from the textbook]

ASM Chart is different from a Flow Chart

- The ASM chart implicitly includes timing info
- It is assumed that the underlying FSM changes from one state to another on every active clock edge
- Flow charts don't make that assumption.

The general model for a sequential circuit



The general model for a sequential circuit

$$M = (W, Z, S, \varphi, \lambda)$$

- W, Z, and S are finite, nonempty sets of inputs, outputs, and states, respectively.
- φ is the state transition function, such that $S(t+1) = \varphi[W(t), S(t)]$.
- λ is the output function, such that $\lambda(t) = \lambda[S(t)]$ for the Moore model and $\lambda(t) = \lambda[W(t), S(t)]$ for the Mealy model.

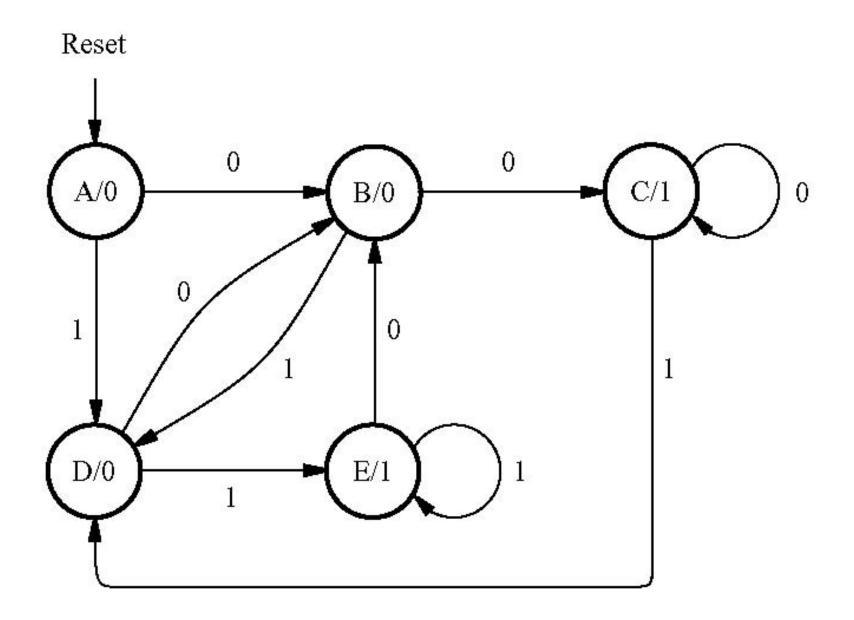
Examples of Solved Problems

Example 6.12

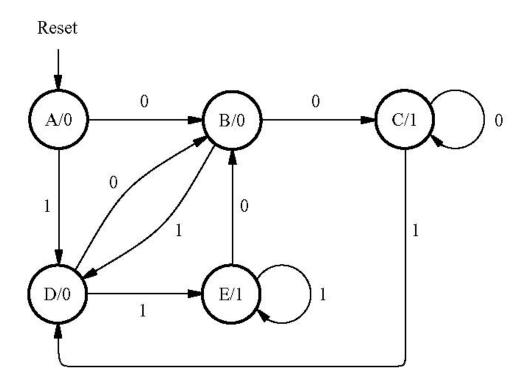
Goal

- Design an FSM that detects if the previous two values of the input w were equal to 00 or 11.
- If either condition is true then the output z should be set to 1; otherwise to 0.

State Diagram



State Table for the FSM



Present	Next state		Output
state	w = 0	w = 1	z
A	В	D	0
В	$^{\mathrm{C}}$	D	0
С	С	D	1
D	В	${ m E}$	0
E	В	\mathbf{E}	1

State Table for the FSM

Present	Next state		Output
state	w = 0	w = 1	z
A	В	D	0
В	$^{\mathrm{C}}$	D	0
С	$^{\mathrm{C}}$	D	1
D	В	${ m E}$	0
E	В	\mathbf{E}	1

Present	Next state		Output
state	w = 0	w = 1	z
A	В	D	0
В	$^{\mathrm{C}}$	D	0
С	$^{\mathrm{C}}$	D	1
D	В	${ m E}$	0
E	В	Ε	1

	Present	Next	Next state	
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Į	000	001	011	0
3	001	010	011	0
\mathbf{C}	010	010	011	1
)	011	001	100	0
Ð	100	001	100	1

	Present	Next	state	
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	001	011	0
В	001	010	011	0
\mathbf{C}	010	010	011	1
D	011	001	100	0
Ε	100	001	100	1

	Present	Next	state	
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	001	011	0
В	001	010	011	0
\mathbf{C}	010	010	011	1
D	011	001	100	0
\mathbf{E}	100	001	100	1

$$z = y_3 + \overline{y}_1 y_2$$

	Present	Next state		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	001	011	0
В	001	010	011	0
\mathbf{C}	010	010	011	1
D	011	001	100	0
\mathbf{E}	100	001	100	1
'	101	ddd	ddd	d
	110	ddd	ddd	d
	111	ddd	ddd	d

Truth Table for the Output z

Present	Next state		
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	011	0
001	010	011	0
010	010	011	1
011	001	100	0
100	001	100	1
101	ddd	ddd	d
110	ddd	ddd	d
111	ddd	ddd	d

<i>y</i> ₃	y_2	y_1	z
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

Truth Table for the Output z

Present	Next	Next state	
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	011	0
001	010	011	0
010	010	011	1
011	001	100	0
100	001	100	1
101	ddd	ddd	d
110	ddd	ddd	d
111	ddd	ddd	d

<i>y</i> ₃	y_2	y_1	z
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	d
1	1	0	d
1	1	1	d

Truth Table for the Output z

	Present	Next	state	
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
	000	001	011	0
	001	010	011	0
ı	010	010	011	1
ı	011	001	100	О
	100	001	100	1
	101	ddd	ddd	d
	110	ddd	ddd	d
	111	ddd	ddd	d

<i>y</i> ₃	y_2	y_1	z
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	d
1	1	0	d
1	1	1	d

K-Map for the Output z

Present		Next	Next state			
	state	w = 0	w = 1	Outp	out	
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z		
Г	000	001	011	0		
П	001	010	011	0		
П	010	010	011	1		
П	011	001	100	0		
П	100	001	100	1		
	101	ddd	ddd	d		
	110	ddd	ddd	d		
	111	ddd	ddd	d		

<i>V</i> 3 <i>Y</i>	2			
	00	01	11	10
0	0	1	d	~
1	0	0	d	d

<i>y</i> ₃	<i>y</i> ₂	y_1	z
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	d
1	1	0	d
1	1	1	d

The Expression for the Output z

Present	Next state		
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	011	0
001	010	011	0
010	010	011	1
011	001	100	0
100	001	100	1
101	ddd	ddd	d
110	ddd	ddd	d
111	ddd	ddd	d

<i>y</i> ₃ <i>y</i> ₂	2	y ₁	y ₂		У
	00	01	11	10	
0	0	1	a	1	
1	0	0	Q	d	

 y_I

y_3	<i>y</i> ₂	<i>y</i> ₁	z
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	d
1	1	0	d
1	1	1	d

State-Assigned Table for the FSM

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	001	011	0
В	001	010	011	0
\mathbf{C}	010	010	011	1
D	011	001	100	0
\mathbf{E}	100	001	100	1

$$Y_1 = w\overline{y}_1\overline{y}_3 + w\overline{y}_2\overline{y}_3 + \overline{w}y_1y_2 + \overline{w}\overline{y}_1\overline{y}_2$$

$$Y_2 = y_1 \overline{y}_2 + \overline{y}_1 y_2 + w \overline{y}_2 \overline{y}_3$$

$$Y_3 = wy_3 + wy_1y_2$$

Truth Table for Y₃

Present	Next		
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	011	0
001	010	011	0
010	010	011	1
011	001	100	0
100	001	100	1
101	ddd	ddd	d
110	ddd	d <mark>dd</mark>	d
111	ddd	<mark>d</mark> dd	d

w	<i>y</i> ₃	y_2	y_1	<i>Y</i> ₃	Y_2	Y_1
0	0	0	0	0		
0	0	0	1	0		
0	0	1	0	0		
0	0	1	1	0		
0	1	0	0	0		
0	1	0	1	d		
0	1	1	0	d		
0	1	1	1	d		
1	0	0	0	0		
1	0	0	1	0		
1	0	1	0	0		
1	0	1	1	1		
1	1	0	0	1		
1	1	0	1	d		
1	1	1	0	d		
1	1	1	1	d		

Truth Table for Y₂

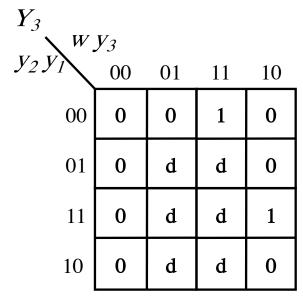
Present	Next		
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	$0\overline{1}\overline{1}$	0
001	010	011	0
010	010	011	1
011	001	100	0
100	001	100	1
101	ddd	d <mark>d</mark> d	d
110	ddd	<mark>d</mark> dd	d
111	<u>d</u> dd	<mark>dd</mark> d	d

w	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_1
0	0	0	0	0	0	
0	0	0	1	0	1	
0	0	1	0	0	1	
0	0	1	1	0	0	
0	1	0	0	0	0	
0	1	0	1	d	d	
0	1	1	0	d	d	
0	1	1	1	d	d	
1	0	0	0	0	1	
1	0	0	1	0	1	
1	0	1	0	0	1	
1	0	1	1	1	0	
1	1	0	0	1	0	
1	1	0	1	d	d	
1	1	1	0	d	d	
1	1	1	1	d	d	

Truth Table for Y₁

Present	Next		
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	011	0
001	010	011	0
010	010	011	1
011	001	10 <mark>0</mark>	0
100	001	100	1
101	ddd	ddd	d
110	ddd	ddd	d
111	<u>dd</u> d	<u>dd</u> d	d

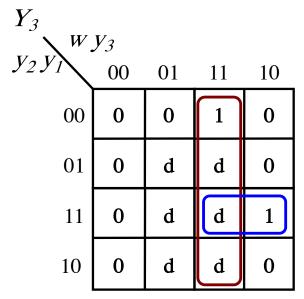
w	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_1
0	0	0	0	0	0	1
0	0	0	1	0	1	0
0	0	1	0	0	1	0
0	0	1	1	0	0	1
0	1	0	0	0	0	1
0	1	0	1	d	d	d
0	1	1	0	d	d	d
0	1	1	1	d	d	d
1	0	0	0	0	1	1
1	0	0	1	0	1	1
1	0	1	0	0	1	1
1	0	1	1	1	0	0
1	1	0	0	1	0	0
1	1	0	1	d	d	d
1	1	1	0	d	d	d
1	1	1	1	d	d	d



Y_2 $W y_3$							
y_2y_1	00	01	11	10			
00	0	0	0	1			
01	1	d	d	1			
11	0	d	d	0			
10	1	d	d	1			

$Y_1 \setminus w y_3$						
y_2y_1	00	01	11	10		
00	1	1	0	1		
01	0	d	d	1		
11	1	d	d	0		
10	0	d	d	1		

w	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_I
0	0	0	0	0	0	1
0	0	0	1	0	1	0
0	0	1	0	0	1	0
0	0	1	1	0	0	1
0	1	0	0	0	0	1
0	1	0	1	d	d	d
0	1	1	0	d	d	d
0	1	1	1	d	d	d
1	0	0	0	0	1	1
1	0	0	1	0	1	1
1	0	1	0	0	1	1
1	0	1	1	1	0	0
1	1	0	0	1	0	0
1	1	0	1	d	d	d
1	1	1	0	d	d	d
1	1	1	1	d	d	d

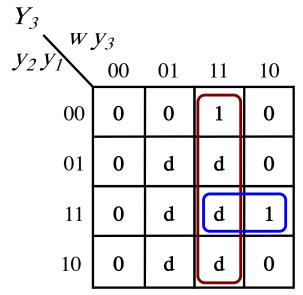


Y_2 $W y_3$							
<i>y</i> ₂ <i>y</i> ₁	00	01	11	10			
00	0	0	0	1			
01	1	d	d	1			
11	0	d	d	0			
10	1	d	d	1			

$Y_1 \setminus w y_3$						
y_2y_1	00	01	11	10		
00	1	1	0	1		
01	0	d	d	1		
11	1	d	d	0		
10	0	d	d	1		

w	<i>y</i> ₃	y_2	y_1	<i>Y</i> ₃	Y_2	Y_1
0	0	0	0	0	0	1
0	0	0	1	0	1	0
0	0	1	0	0	1	0
0	0	1	1	0	0	1
0	1	0	0	0	0	1
0	1	0	1	d	d	d
0	1	1	0	d	d	d
0	1	1	1	d	d	d
1	0	0	0	0	1	1
1	0	0	1	0	1	1
1	0	1	0	0	1	1
1	0	1	1	1	0	0
1	1	0	0	1	0	0
1	1	0	1	d	d	d
1	1	1	0	d	d	d
1	1	1	1	d	d	d

Expressions for Y₃, Y₂, Y₁



Y_2 $W y_3$						
y_2y_1	00	01	11	10		
00	0	0	0	1		
01	1	d	d	1		
11	0	d	d	0		
10	1	d	d	1		

w	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_1
0	0	0	0	0	0	1
0	0	0	1	0	1	0
0	0	1	0	0	1	0
0	0	1	1	0	0	1
0	1	0	0	0	0	1
0	1	0	1	d	d	d
0	1	1	0	d	d	d
0	1	1	1	d	d	d
1	0	0	0	0	1	1
1	0	0	1	0	1	1
1	0	1	0	0	1	1
1	^	1	1	1	0	^

$$Y_1 = w\overline{y}_1\overline{y}_3 + w\overline{y}_2\overline{y}_3 + \overline{w}y_1y_2 + \overline{w}y_1\overline{y}_2$$

$$Y_2 = y_1\overline{y}_2 + \overline{y}_1y_2 + w\overline{y}_2\overline{y}_3$$

$$Y_3 = wy_3 + wy_1y_2$$

Next State and Output Expressions

$$Y_1 = w\overline{y}_1\overline{y}_3 + w\overline{y}_2\overline{y}_3 + \overline{w}y_1y_2 + \overline{w}y_1\overline{y}_2$$

$$Y_2 = y_1\overline{y}_2 + \overline{y}_1y_2 + w\overline{y}_2\overline{y}_3$$

$$Y_3 = wy_3 + wy_1y_2$$

$$z = y_3 + \overline{y}_1 y_2$$

Present	Next	state	
state	w = 0	w = 1	Output
$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
000	001	011	0
001	010	011	0
010	010	011	1
011	001	100	0
100	001	100	1

 \mathbf{E}

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	100	110	0
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
\mathbf{E}	111	100	111	1

B,C, D, E – when $y_3=1$

Present	Next	Next state		
state	w = 0	w = 1	z	
A	В	D	0	
В	$^{\mathrm{C}}$	D	0	
С	$^{\mathrm{C}}$	D	1	
D	В	${ m E}$	0	
Ε	В	\mathbf{E}	1	

Present	Next	Next state		
state	w = 0	w = 1	z	
A	В	D	0	
В	С	D	0	
$^{\mathrm{C}}$	С	D	1	
D	В	${ m E}$	0	
\mathbf{E}	В	\mathbf{E}	1	

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	001	011	0
В	001	010	011	0
\mathbf{C}	010	010	011	1
D	011	001	100	0
Е	100	001	100	1

	Present	Next state		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
В	100	101	110	0
С	101	101	110	1
D	110	100	111	0
Ε	111	100	111	1

B,C, D, E – when $y_3=1$

	Present	Next	state	
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
\mathbf{E}	111	100	111	1

	Present	Next	state		
	state	w = 0	w = 1	Output	
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z	
Α	000	100	110	0	cut here
В	100	101	110	0	—— cat here
\mathbf{C}	101	101	110	1	
D	110	100	111	0	
\mathbf{E}	111	100	111	1	

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	100	110	0
	001	ddd	ddd	d
	010	ddd	ddd	d
	011	ddd	ddd	d
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
\mathbf{E}	111	100	111	1

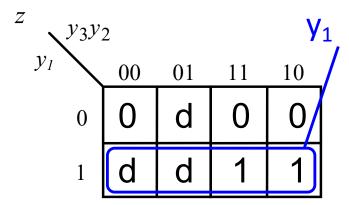
Truth Table for the Output z

	Present state		Next state		0-11			
			w = 0	w = 1	О	utp	ut	
	$y_3y_2y_1$		$Y_3Y_2Y_1$	$Y_3Y_2Y_1$		z		
A		000		100	110		0	
		001		ddd	ddd		d	
		010		ddd	ddd		d	
		011		ddd	ddd		d	
В		100		101	110		0	
$^{\rm C}$		101		101	110		1	
D		110		100	111		0	
\mathbf{E}		111		100	111		1	

y_3	y_2	y_1	z
0	0	0	0
0	0	1	d
0	1	0	d
0	1	1	d
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Expression for the Output z

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
	001	ddd	ddd	d
	010	ddd	ddd	d
	011	ddd	ddd	d
В	100	101	110	0
С	101	101	110	1
D	110	100	111	0
Ε	111	100	111	1



<i>y</i> ₃	y_2	y_1	z
0	0	0	0
0	0	1	d
0	1	0	d
0	1	1	d
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Truth Table for Y₃

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
	001	ddd	d <mark>dd</mark>	d
	010	ddd	d <mark>dd</mark>	d
	011	<u>d</u> dd	<mark>d</mark> dd	d
В	100	101	1 10	0
$^{\rm C}$	101	101	110	1
D	110	100	111	0
Ε	111	100	<u>1</u> 11	1

W	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_I
0	0	0	0	1		
0	0	0	1	d		
0	0	1	0	d		
0	0	1	1	d		
0	1	0	0	1		
0	1	0	1	1		
0	1	1	0	1		
0	1	1	1	1		
1	0	0	0	1		
1	0	0	1	d		
1	0	1	0	d		
1	0	1	1	d		
1	1	0	0	1		
1	1	0	1	1		
1	1	1	0	1		
1	1	1	1	1		

Truth Table for Y₂

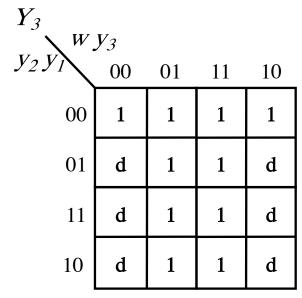
	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
	001	ddd	d <mark>d</mark> d	d
	010	<mark>dd</mark> d	<mark>d</mark> d	d
	011	ddd	<mark>dd</mark> d	d
В	100	101	1 10	0
$^{\rm C}$	101	101	110	1
D	110	100	111	0
Ε	111	1 <u>0</u> 0	$1\overline{1}1$	1

w	<i>y</i> ₃	y_2	<i>y</i> ₁	<i>Y</i> ₃	Y_2	Y_1
0	0	0	0	1	0	
0	0	0	1	d	d	
0	0	1	0	d	d	
0	0	1	1	d	d	
0	1	0	0	1	0	
0	1	0	1	1	0	
0	1	1	0	1	0	
0	1	1	1	1	0	
1	0	0	0	1	1	
1	0	0	1	d	d	
1	0	1	0	d	d	
1	0	1	1	d	d	
1	1	0	0	1	1	
1	1	0	1	1	1	
1	1	1	0	1	1	
1	1	1	1	1	1	

Truth Table for Y₁

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
	001	ddd	ddd	d
	010	ddd	ddd	d
	011	ddd	ddd	d
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
Ε	111	100	$11\overline{1}$	1

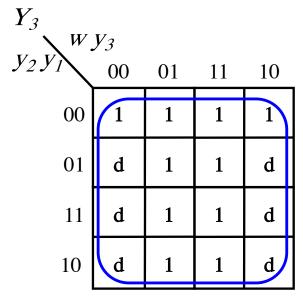
w	<i>y</i> ₃	<i>y</i> ₂	y_1	Y_3	Y_2	Y_{I}
0	0	0	0	1	0	0
0	0	0	1	d	d	d
0	0	1	0	d	d	d
0	0	1	1	d	d	d
0	1	0	0	1	0	1
0	1	0	1	1	0	1
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	1	1	0
1	0	0	1	d	d	d
1	0	1	0	d	d	d
1	0	1	1	d	d	d
1	1	0	0	1	1	0
1	1	0	1	1	1	0
1	1	1	0	1	1	1
1	1	1	1	1	1	1



Y_2 W .	<i>y</i> ₃			
y_2y_1	00	01	11	10
00	0	0	1	1
01	d	0	1	d
11	d	0	1	d
10	d	0	1	d

$Y_1 \setminus w y_3$						
y_2y_1	00	01	11	10		
00	0	1	0	0		
01	d	1	0	d		
11	d	0	1	d		
10	d	0	1	d		

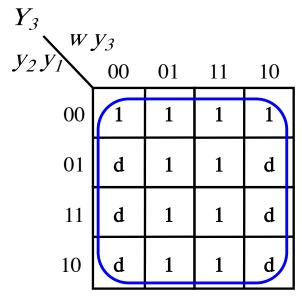
w	<i>y</i> ₃	y_2	y_1	<i>Y</i> ₃	Y_2	Y_{I}
0	0	0	0	1	0	0
0	0	0	1	d	d	d
0	0	1	0	d	d	d
0	0	1	1	d	d	d
0	1	0	0	1	0	1
0	1	0	1	1	0	1
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	1	1	0
1	0	0	1	d	d	d
1	0	1	0	d	d	d
1	0	1	1	d	d	d
1	1	0	0	1	1	0
1	1	0	1	1	1	0
1	1	1	0	1	1	1
1	1	1	1	1	1	1



Y_2 $W y_3$								
<i>y</i> ₂ <i>y</i> ₁	00	01	11	10				
00	0	0	1	1				
01	d	0	1	d				
11	d	0	1	d				
10	d	0	1	d				

$Y_1 \setminus W y_3$								
y_2y_1	00	01	11	10				
00	0	1	0	0				
01	d	1	0	d				
11	d	0	1	d				
10	d	0	1	d				

w	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_{I}
0	0	0	0	1	0	0
0	0	0	1	d	d	d
0	0	1	0	d	d	d
0	0	1	1	d	d	d
0	1	0	0	1	0	1
0	1	0	1	1	0	1
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	1	1	0
1	0	0	1	d	d	d
1	0	1	0	d	d	d
1	0	1	1	d	d	d
1	1	0	0	1	1	0
1	1	0	1	1	1	0
1	1	1	0	1	1	1
1	1	1	1	1	1	1



Y_2 wy_3								
<i>y</i> ₂ <i>y</i> ₁	00	01	11	10				
00	0	0	1	1				
01	d	0	1	d				
11	d	0	1	d				
10	d	0	1	d				

Y	? •	Y ₂	. }	1		w	<i>y</i> ₃	y_2	y_1	Y_3	Y_2	Y_1
_	3,		7			0	0	0	0	1	0	0
						0	0	0	1	d	d	d
						0	0	1	0	d	d	d
						0	0	1	1	d	d	d
						0	1	0	0	1	0	1
\mathbb{V} .	<i>y</i> ₃					0	1	0	1	1	0	1
	00	01	11	10	1	0	1	1	0	1	0	0
00	0	0	1	1		0	1	1	1	1	0	0
01	d	0	1	d		1	0	0	0	1	1	0
11	d	0	1	d		1	0	0	1	d	d	d
10	d	0	1	d		1	0	1	0	d	d	d
,						1	0	1	1	d	d	d
)	$Y_1 = wy_2 + \overline{w}y_3\overline{y}_2$						2	0	0	1	1	0
$Y_2 = w$						0	1	1	1	0		
							1	0	1	1	1	
	Y_3 :	=]	L			L	 	1	1	1	1	1

$Y_1 \setminus w y_3$								
y_2y_1	00	01	11	10				
00	0	1	0	0				
01	d	1	0	d				
11	d	0	1	d				
10	d	0	1	d				

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	100	110	0
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
Ε	111	100	111	1

$$Y_1 = wy_2 + \overline{w}y_3\overline{y}_2$$

$$Y_2 = w$$

$$Y_3 = 1$$

$$z = y_1$$

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
Α	000	100	110	0
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
\mathbf{E}	111	100	111	1

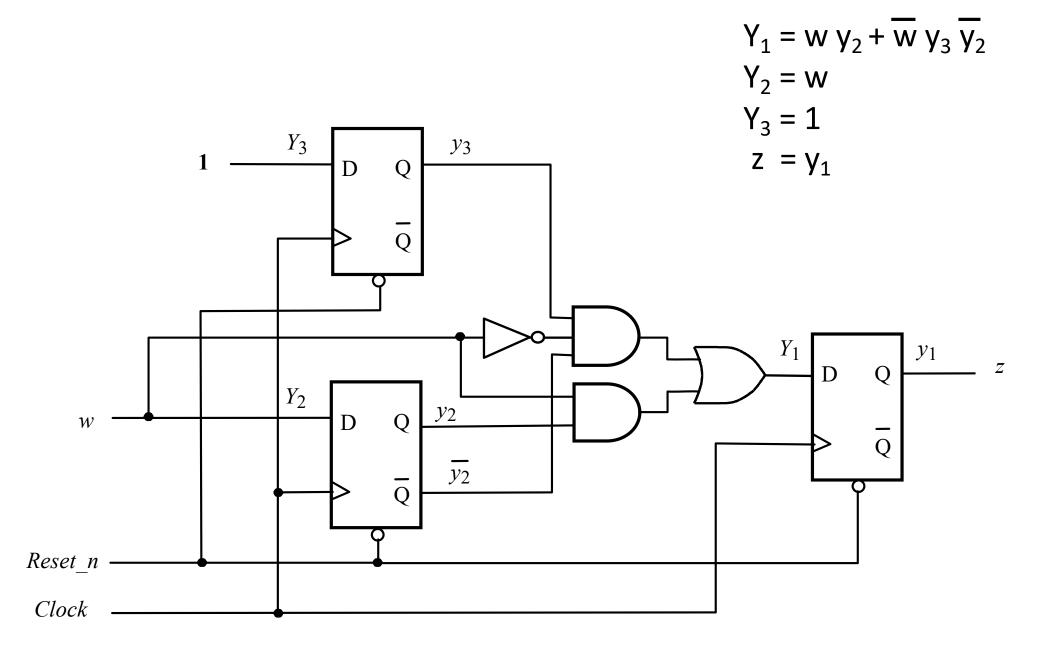
$$Y_1 = wy_2 + \overline{w}y_3\overline{y}_2$$

$$Y_2 = w$$

$$Y_3 = 1$$

$$z = y_1$$

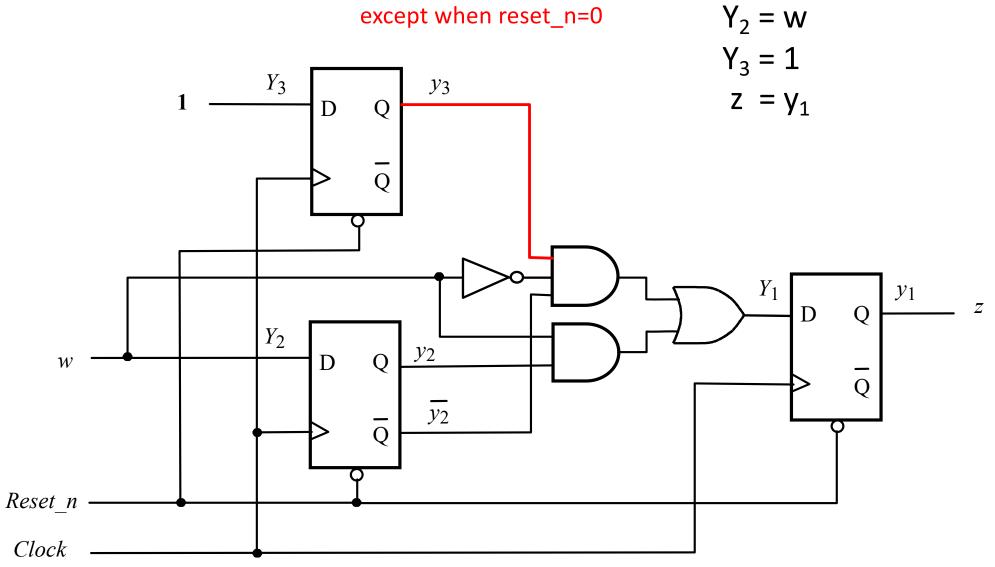
The Circuit Diagram



The Circuit Diagram

 $Y_1 = w y_2 + \overline{w} y_3 y_2$

this is always 1, except when reset_n=0



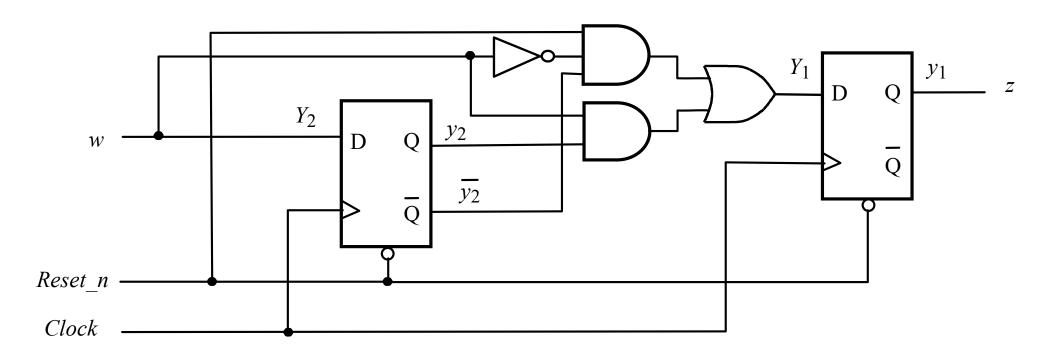
The Circuit Diagram

$$Y_1 = w y_2 + \overline{w} y_3 \overline{y}_2$$

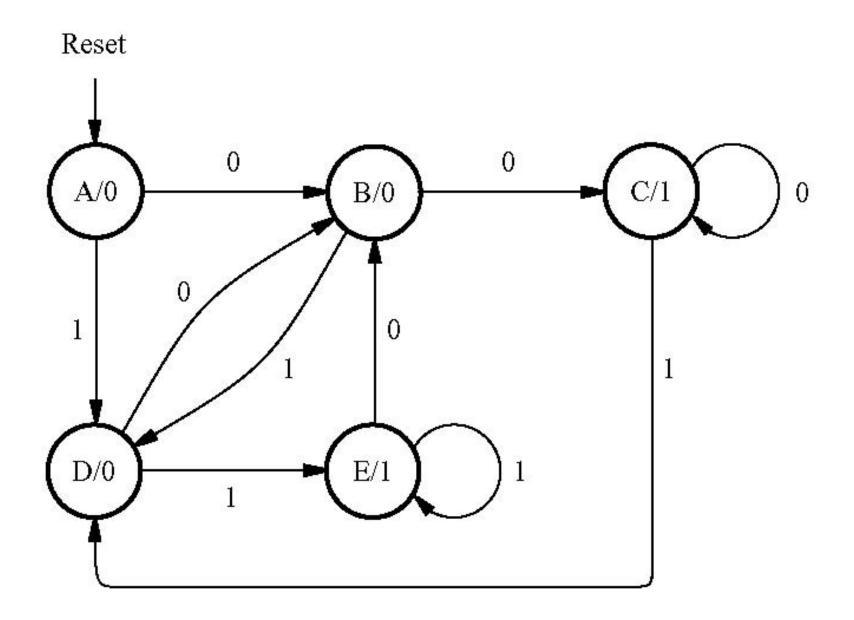
$$Y_2 = w$$

$$Y_3 = 1$$

$$z = y_1$$



State Diagram



Example 6.13

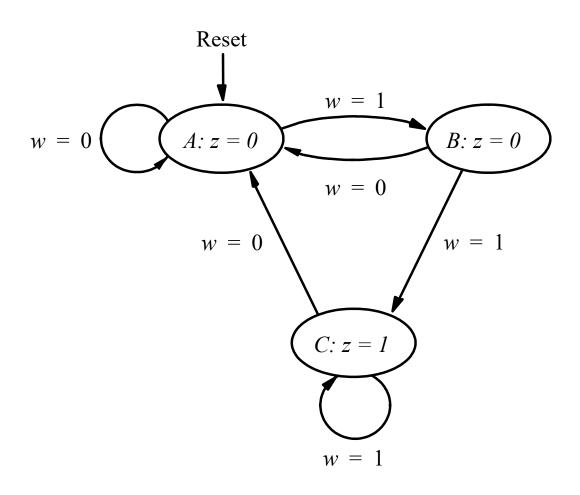
Goal

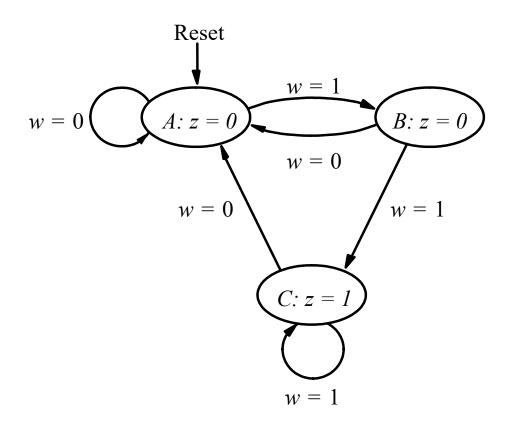
- Design an FSM that detects if the previous two values of the input w were equal to 00 or 11.
- But do this with two different FSMs. The first one detects two consecutive 1's. The second one detects two consecutive 0's.
- If either condition (i.e., output of FSM) is true then the output z should be set to 1; otherwise to 0.

Example 6.13

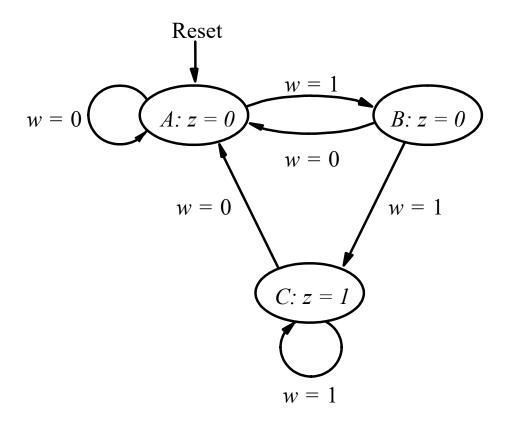
(Construct the first FSM)

FSM to detect two consecutive 1's (this was the first example in Chapter 6)





Present state	Next state $w = 0 w = 1$	Output z
A B C		



Present	Next	Output	
state	w = 0	w = 1	Z
A	A	В	0
В	A	C	0
C	A	C	1

Figure 6.4 from the textbook]

A Better State Encoding

Present	Next	Output	
state	w = 0	w = 1	\overline{z}
A	A	В	0
В	A	C	0
C	A	C	1

Suppose we encoded our states another way:

$$A \sim 00$$

$$B \sim 01$$

A Better State Encoding

Present	Next state		Output
state	w = 0	w = 1	Z
A	A	В	0
В	A	C	0
C	A	C	1

$A \sim 00$
$B \sim 01$
C ~ 11

Present		Next state		
	state	w = 0	w = 1	Output
				Z

A Better State Encoding

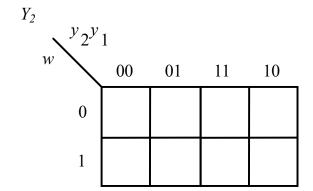
Present	Next state		Output
state	w = 0	w = 1	Z
A	A	В	0
В	A	C	0
C	A	C	1

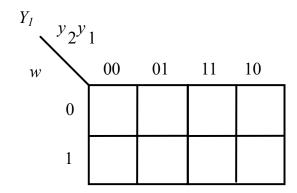
	Present	Next state		
	state	w = 0	w = 1	Output
	<i>y</i> 2 <i>y</i> 1	Y_2Y_1	Y_2Y_1	Z
A	00	00	01	0
В	01	00	11	0
\mathbf{C}	11	00	11	1
	10	dd	dd	d

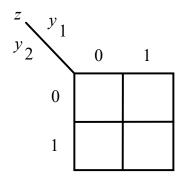
	Present	Next state		
	state	w = 0	w = 1	Output
	<i>y</i> 2 <i>y</i> 1	Y_2Y_1	Y_2Y_1	Z
A	00	00	01	0
В	01	00	11	0
C	11	00	11	1
	10	dd	dd	d

Warning: This table does not enumerate y_2y_1 , in the standard way, so be careful when filling out the K-Map.

	Present	Next state		
	state	w = 0	w = 1	Output
	<i>y</i> 2 <i>y</i> 1	$Y_2 Y_1$	Y_2Y_1	Z
A	00	00	01	0
В	01	00	11	0
C	11	00	11	1
	10	dd	dd	d



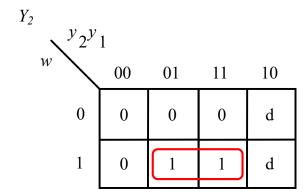




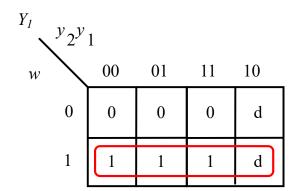
Next state

Warning: This table does not enumerate y_2y_1 , in the standard way, so be careful when filling out the K-Map.

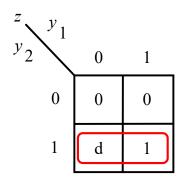
Present Output w = 0w = 1state Z $Y_2 Y_1$ $Y_2 Y_1$ *y*2*y*1 01 00 000 В 0 01 00 11 11 00 11 d 10 dddd



$Y_2(w,$	V_2 ,	v_1)	=	WV_1
<u> </u>		J 1/		√ 1

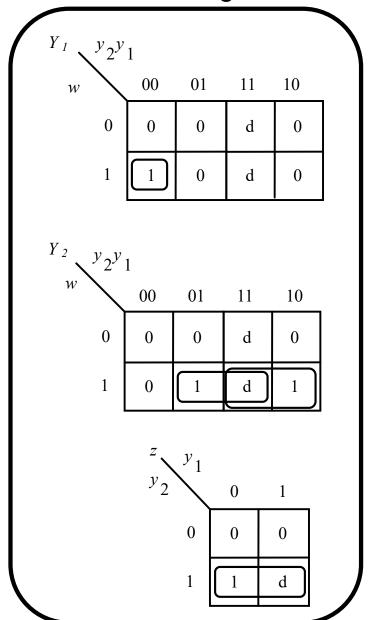


$$Y_1(w, y_2, y_1) = w$$

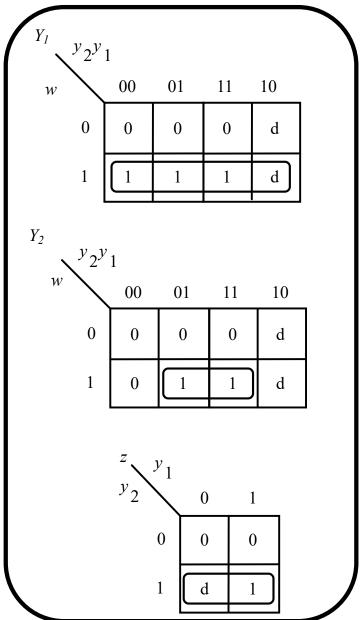


$$z(y_2, y_1) = y_2$$

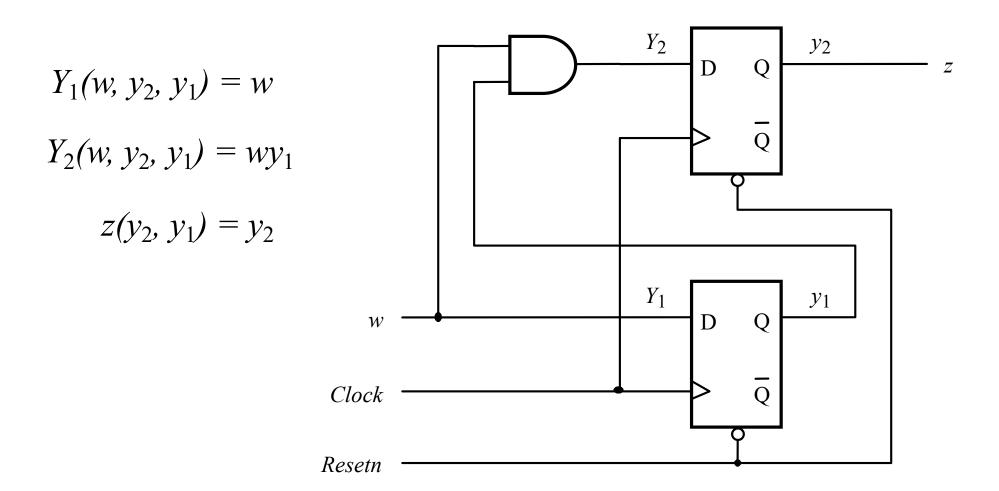
Original State Encodings



New State Encodings



The Circuit Diagram

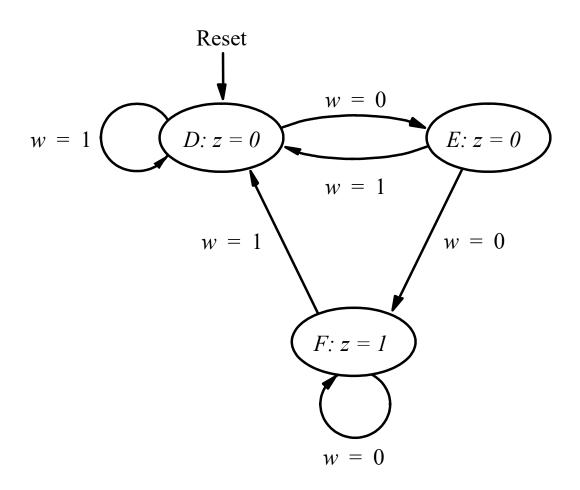


[Figure 6.17 from the textbook]

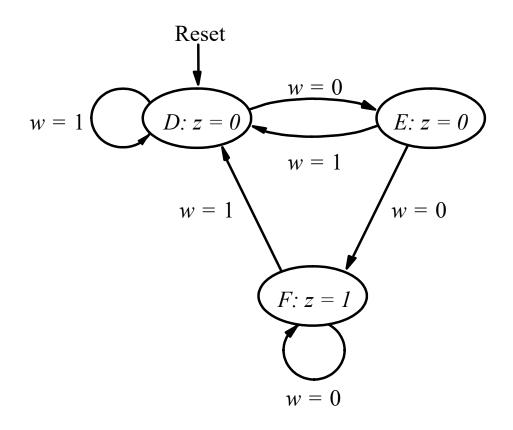
Example 6.13

(Construct the second FSM)

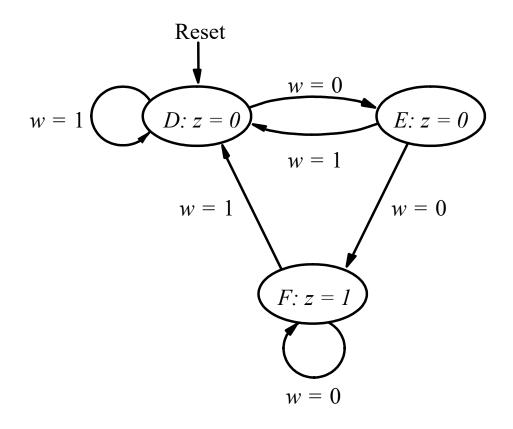
FSM to detect two consecutive 0's



This is similar to the previous one. Just invert the w's and relabel the states to D,E,F.



Present	Next state	Output
state	w = 0 $w = 1$	2
D		
E		
F		



Present	Next state		Output
state	w = 0	w = 1	Z
D	Е	D	0
Е	F	D	0
F	F	D	1

FSM that detects a sequence of two zeros

Present	Ne xt	Output	
state	w = 0	w = 1	z_{zeros}
D	E	D	0
E	\mathbf{F}	D	0
F	\mathbf{F}	D	1

(a) State table

	Present	Next state			
	state	w = 0	w = 1	Output	
	y_4y_3	Y_4Y_3	Y_4Y_3	z_{zeros}	
D [00	01	00	0	
\mathbf{E}	01	11	00	0	
\mathbf{F}	11	11	00	1	
	10	dd	dd	d	

FSM that detects a sequence of two zeros

Present	Ne xt state		Output
state	w = 0	w = 1	z_{zeros}
D	E	D	0
${ m E}$	F ←	\rightarrow D	0
F	\mathbf{F}	D	1

Only these two columns are swapped relative to the first FSM. And the states have different names now.

(a) State table

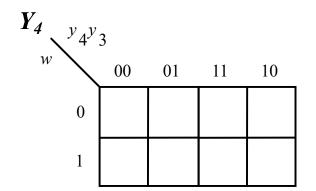
	Present	Next	state	
	state	w = 0	w = 1	Output
	y_4y_3	Y_4Y_3	Y_4Y_3	z_{zeros}
D	00	01	00	0
E	01	11	00	0
F	11	11	00	1
	10	dd	dd	d

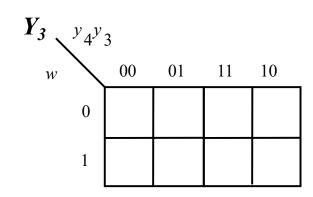
Only these two columns are swapped relative to the first FSM.

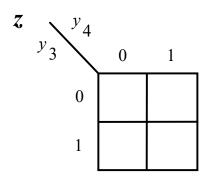
[Figure 6.90 from the textbook]

	Present	Next state		
	state	w = 0	w = 1	Output
	<i>y</i> 4 <i>y</i> 3	Y_4Y_3	Y_4Y_3	Z
D	00	01	00	0
E	01	11	00	0
F	11	11	00	1
	10	dd	dd	d

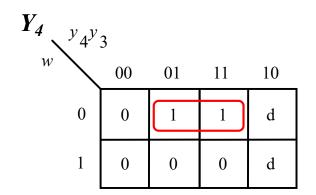
	Present	Next state		
	state	w = 0	w = 1	Output
	<i>y</i> 4 <i>y</i> 3	Y_4Y_3	Y_4Y_3	Z
D	00	01	00	0
E	01	11	00	0
F	11	11	00	1
	10	dd	dd	d

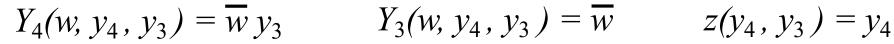


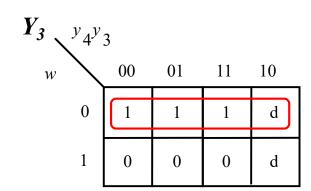




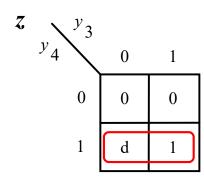
	Present	Next state		
	state	w = 0	w = 1	Output
	<i>y</i> 4 <i>y</i> 3	Y_4Y_3	Y_4Y_3	Z
D	00	01	00	0
E	01	11	00	0
F	11	11	00	1
	10	dd	dd	d





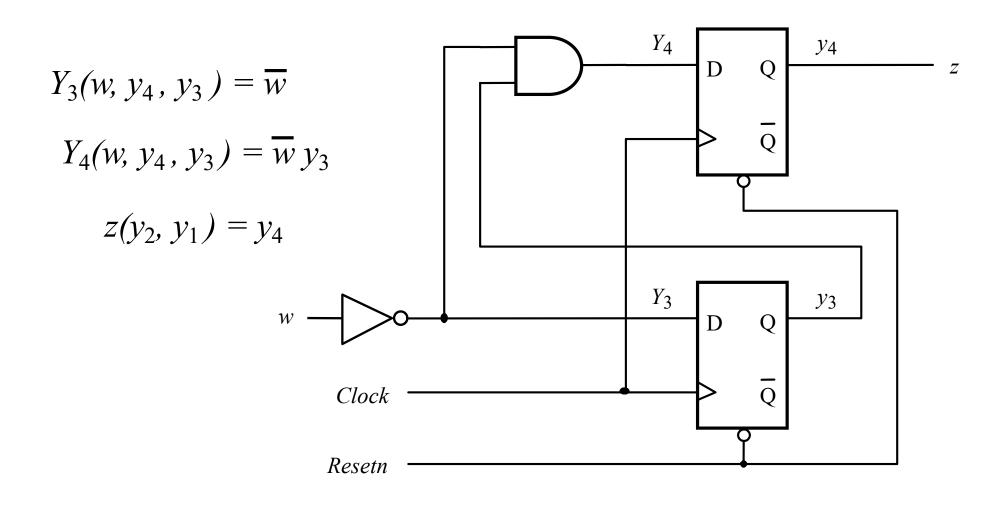


$$Y_3(w, y_4, y_3) = \overline{w}$$



$$z(y_4, y_3) = y_4$$

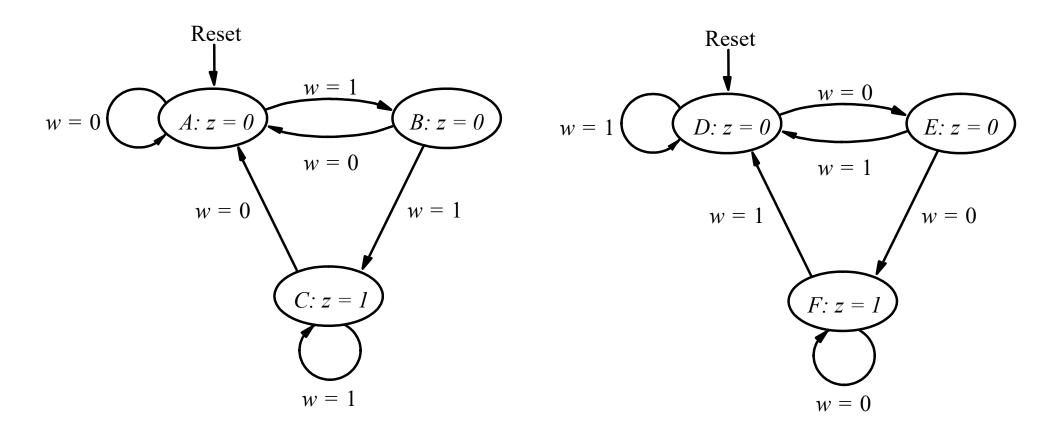
The Circuit Diagram



Example 6.13

(Combine the two FSMs)

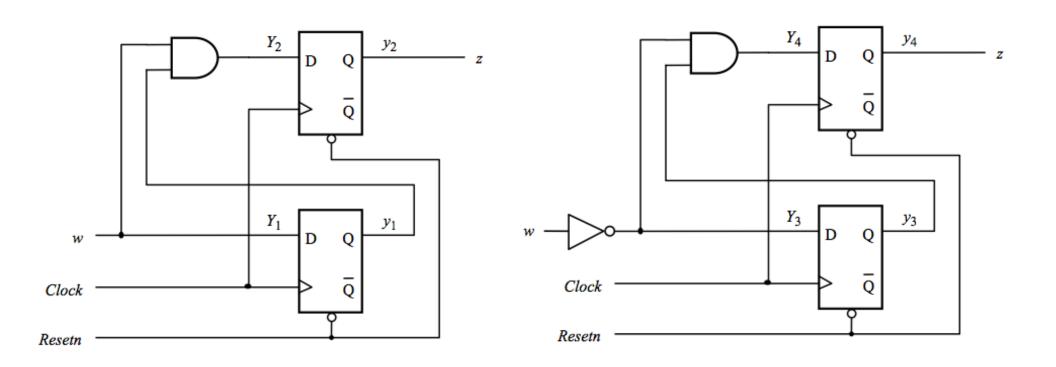
The Two FSMs



Detect two consecutive 1's

Detect two consecutive 0's

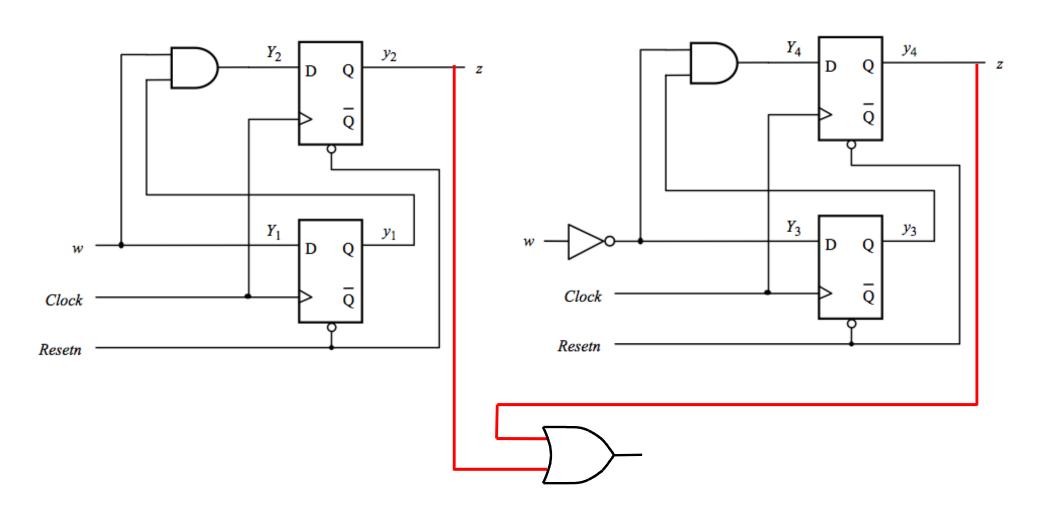
The Two Circuit Diagrams



Detect two consecutive 1's

Detect two consecutive 0's

The Combined Circuit Diagram



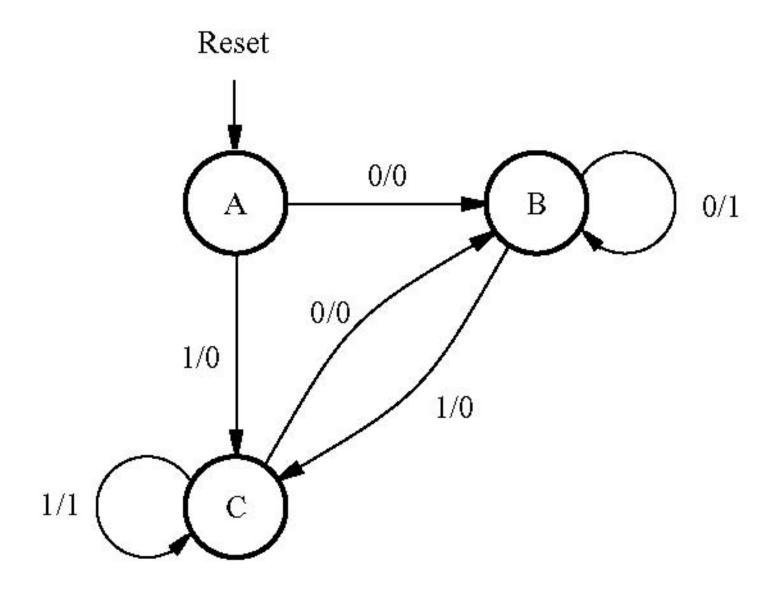
Detect two consecutive 1's or two consecutive 0's

Example 6.14

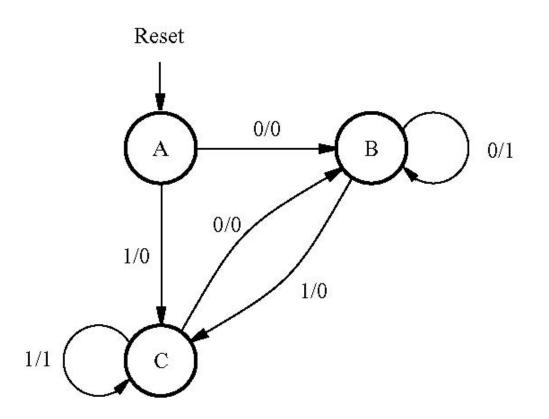
Goal

- Design an FSM that detects if the previous two values of the input w were equal to 00 or 11.
- If either condition is true then the output z should be set to 1; otherwise to 0.
- Implement this as a Mealy-type machine

State Diagram



Building the State Table



Present	Next	state	Output z	
state	w = 0	w = 1	w = 0	w = 1
A	В	$^{\mathrm{C}}$	0	0
В	В	$^{\mathrm{C}}$	1	0
С	В	$^{\mathrm{C}}$	0	1

State Table

Present	Next	state	Output z	
state	w = 0	w = 1	w = 0	w = 1
A	В	С	0	0
В	В	\mathbf{C}	1	0
С	В	С	0	1

Building the State-Assigned Table

Present	Next state		Output z	
state	w = 0	w = 1	w = 0	w = 1
A	В	\mathbf{C}	0	0
В	В	\mathbf{C}	1	0
С	В	\mathbf{C}	0	1

Present	Next state		Output	
state	w = 0	w = 1	w = 0	w = 1
$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z
00	01	11	0	0
01	01	11	1	0
11	01	11	0	1

A B C

	Present	Next state		Output	
	state	w = 0	w = 1	w = 0	w = 1
	$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z
Α	00	01	11	0	0
В	01	01	11	1	0
С	11	01	11	0	1

	Present	sent Next state		Output		
	state	w = 0	w = 1	w = 0	w = 1	
	$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z	
Α	00	01	11	0	0	
В	01	01	11	1	0^{cut}	here
С	11	01	11	0	1	

	Present	Next state		Output	
	state	w = 0	w = 1	w = 0	w = 1
	$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z
A	00	01	11	0	0
В	01	01	11	1	0
С	11	01	11	0	1

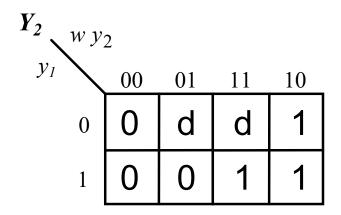
	Present	Next state		Output	
	state	w = 0	w = 1	w = 0	w = 1
	$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z
A	00	01	11	0	0
В	01	01	11	1	0
·	1 0	d d	d d	d	d
\mathbf{C}	11	01	11	0	1

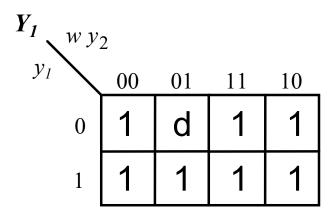
Truth Table for Y₂, Y₁, and z

	Present	Next state		Output	
	state	w = 0	w = 1	w = 0	w = 1
	y_2y_1	Y_2Y_1	Y_2Y_1	z	z
A	00	01	11	0	0
В	01	01	11	1	0
	1 0	d d	d d	d	d
\mathbf{C}	11	01	11	0	1

w	y_2	y_1	Y_2	Y_1	z
0	0	0	0	1	0
0	0	1	0	1	1
0	1	0	d	d	d
0	1	1	0	1	0
1	0	0	1	1	0
1	0	1	1	1	0
1	1	0	d	1	d
1	1	1	1	1	1

K-Maps for Y_2 , Y_1 , and z

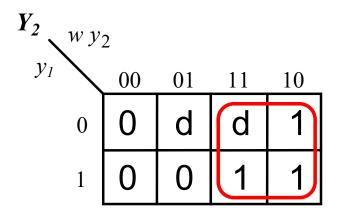




z $w y_2$						
y_1	00	01	11	10		
0	0	d	d	0		
1	1	0	1	0		

w	y_2	y_1	Y_2	Y_1	z
0	0	0	0	1	0
0	0	1	0	1	1
0	1	0	d	d	d
0	1	1	0	1	0
1	0	0	1	1	0
1	0	1	1	1	0
1	1	0	d	1	d
1	1	1	1	1	1

K-Maps for Y_2 , Y_1 , and z



$$Y_2 = w$$

Y_1 $w y_2$					
y_I	00	01	11	10	
0	1	d	1	1	
1	1	1	1	1	

$$Y_1 = 1$$

w	y_2	y_1	Y_2	Y_1	z
0	0	0	0	1	0
0	0	1	0	1	1
0	1	0	d	d	d
0	1	1	0	1	0
1	0	0	1	1	0
1	0	1	1	1	0
1	1	0	d	1	d
1	1	1	1	1	1

$$z = \overline{w} y_1 \overline{y}_2 + w y_2$$

	Present	Next state		Output	
	state	w = 0	w = 1	w = 0	w = 1
	$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z
A	00	01	11	0	0
В	01	01	11	1	0
\mathbf{C}	11	01	11	0	1

$$Y_1 = 1$$

$$Y_2 = w$$

$$z = \overline{w} y_1 \overline{y}_2 + w y_2$$

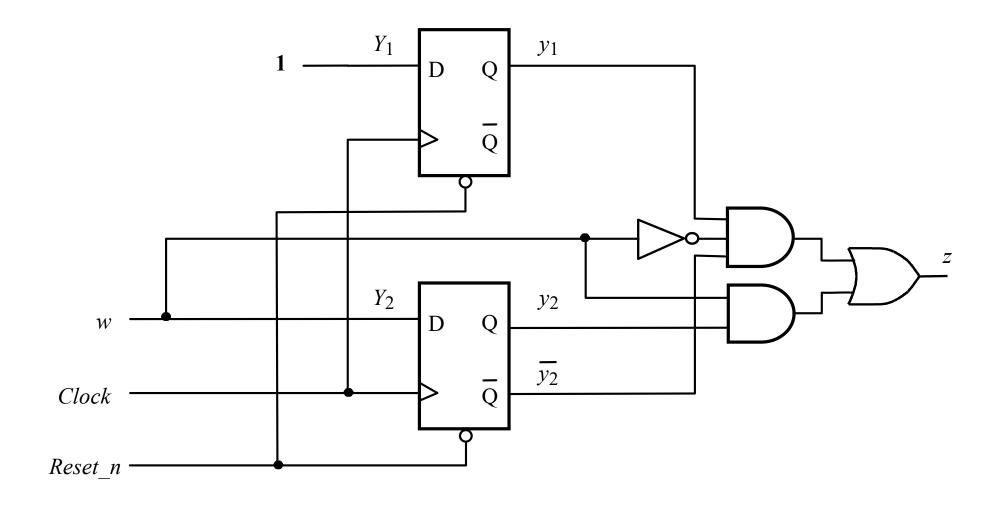
	Present	Next state		Output	
	state	w = 0	w = 1	w = 0	w = 1
	$y_{2}y_{1}$	Y_2Y_1	Y_2Y_1	z	z
	00	01	11	0	0
3	01	01	11	1	0
7	11	01	11	0	1

$$Y_1 = 1$$

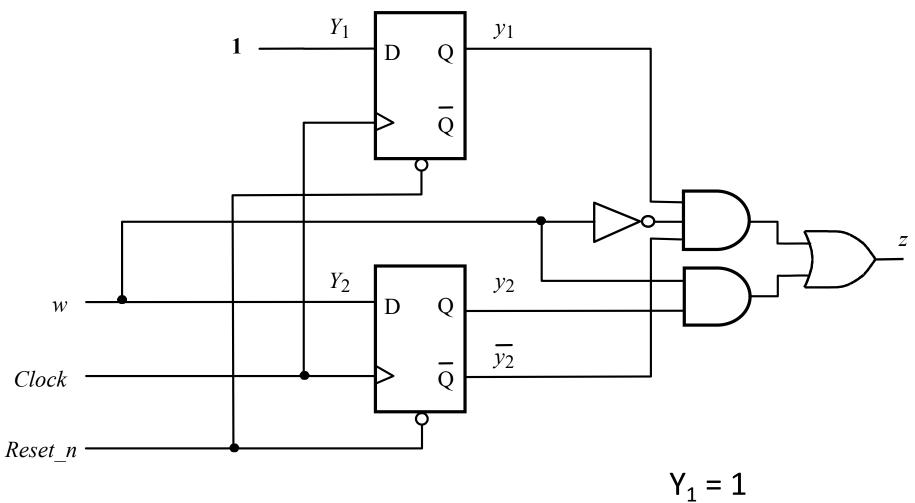
$$Y_2 = W$$

$$z = \overline{W} y_1 \overline{y}_2 + W y_2$$

The Circuit Diagram



The Circuit Diagram

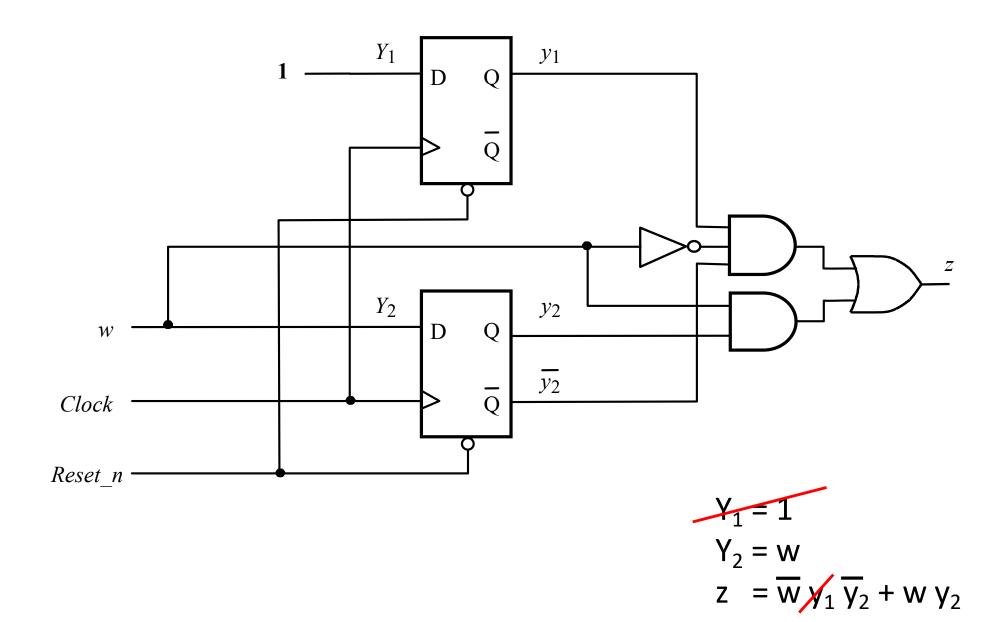


$$Y_1 = 1$$

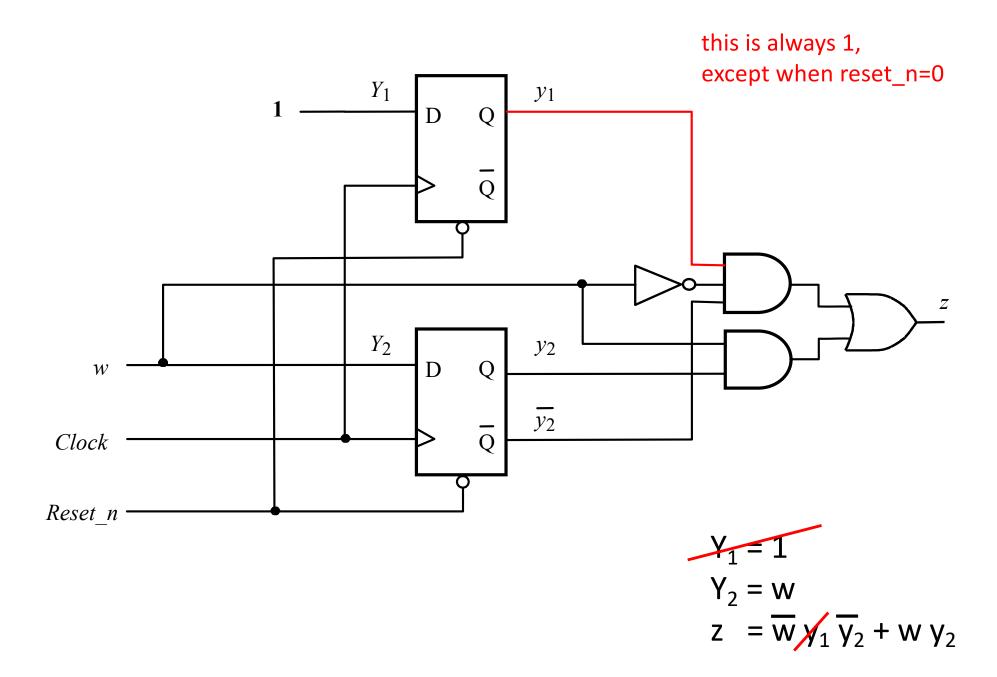
$$Y_2 = w$$

$$z = \overline{w} y_1 \overline{y}_2 + w y_2$$

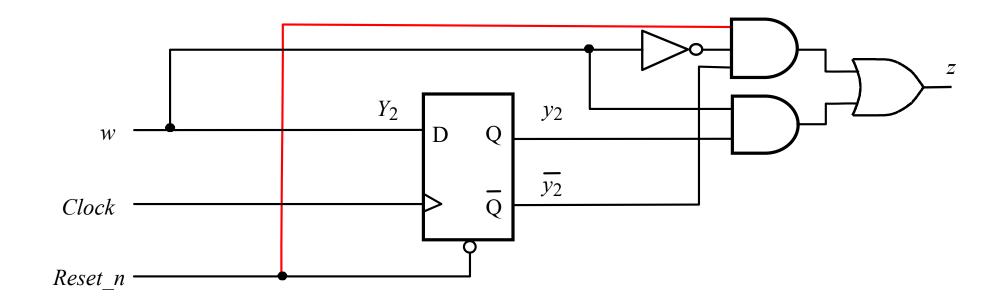
The Circuit Diagram



The Circuit Diagram



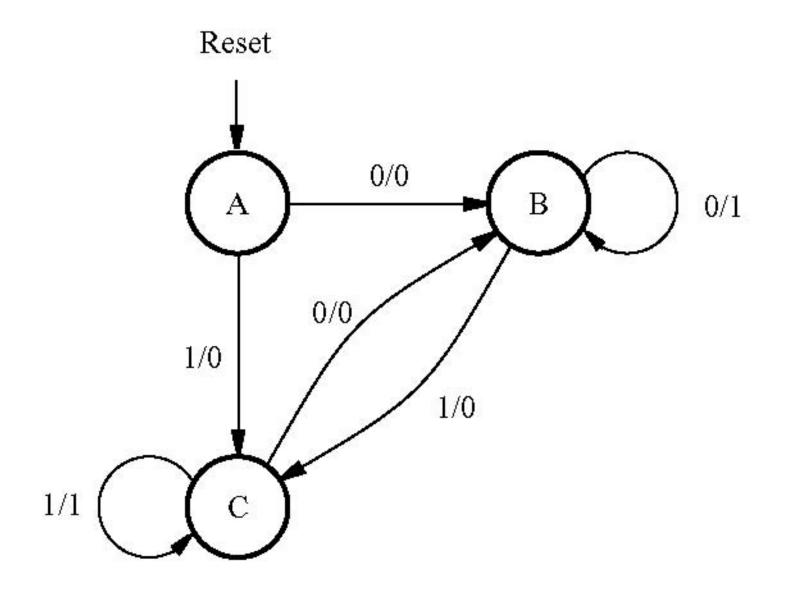
The Simplified Circuit Diagram



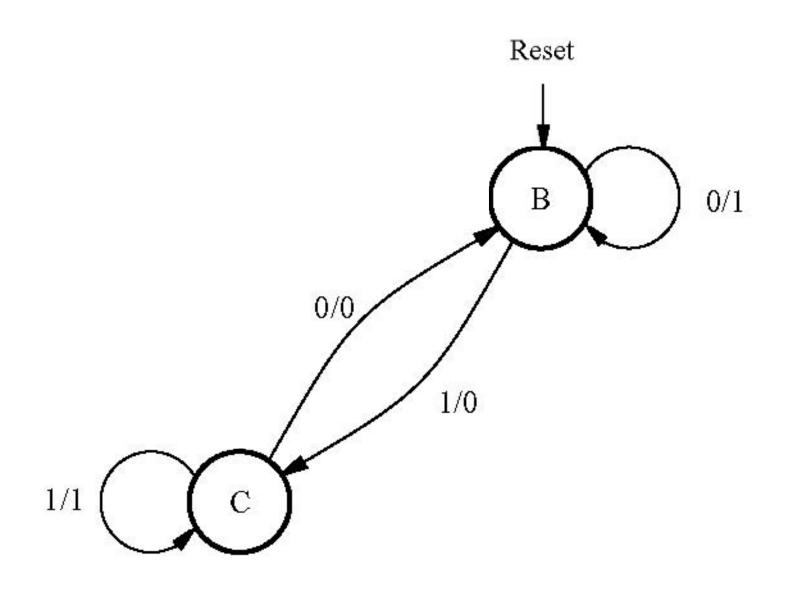
$$Y_2 = W$$

 $z = \overline{W} \overline{y}_2 + W y_2$

Original State Diagram



New State Diagram



Example 6.15

Goal

Implement this state-assigned Table using JK flip-flops

	Present	Next		
	state	w = 0	w = 1	Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	$Y_3Y_2Y_1$	z
A	000	100	110	0
В	100	101	110	0
\mathbf{C}	101	101	110	1
D	110	100	111	0
Ε	111	100	111	1

	Present Flip-flop inputs									Output
	state		w = 0				w = 1			
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
$^{\mathrm{C}}$	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

$Q(t) \rightarrow Q(t+1)$	JK
0 → 0	0 d
0 → 1	1 d
$1 \rightarrow 0$	d 1
1 → 1	d 0

	Present	ent Flip-flop inputs								
	state		w =	= 0		w = 1				Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	00 <mark>0</mark>	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	$\overline{1d}$	110	d0	1d	0d	0
\mathbf{C}	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

$$\begin{array}{c|c}
Q(t) \rightarrow Q(t+1) & J K \\
\hline
0 \rightarrow 0 & 0 d \\
0 \rightarrow 1 & 1 d \\
1 \rightarrow 0 & d 1 \\
1 \rightarrow 1 & d 0
\end{array}$$

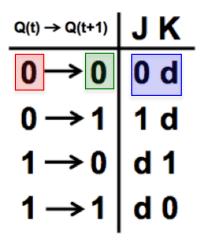
	Present	Flip-flop inputs								
	state		w = 0				w = 1			
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	$10\overline{1}$	d0	0d	1d	110	d0	1d	0d	0
$^{\rm C}$	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

$$\begin{array}{c|ccc}
Q(t) \rightarrow Q(t+1) & J & K \\
\hline
0 \rightarrow 0 & 0 & d \\
\hline
0 \rightarrow 1 & 1 & d \\
1 \rightarrow 0 & d & 1 \\
1 \rightarrow 1 & d & 0
\end{array}$$

	Present	Flip-flop inputs								
	state $w = 0$						w = 1			
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
$^{\rm C}$	$10\overline{1}$	$10\overline{1}$	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
E	111	100	d0	d1	d1	111	d0	d0	d0	1

$$\begin{array}{c|ccc}
Q(t) \rightarrow Q(t+1) & J K \\
\hline
0 \rightarrow 0 & 0 d \\
0 \rightarrow 1 & 1 d \\
1 \rightarrow 0 & d 1 \\
\hline
1 \rightarrow 1 & d 0
\end{array}$$

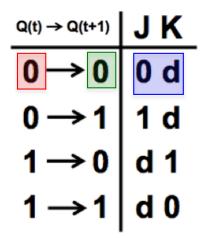
	Present	Flip-flop inputs								
	state	w = 0				w = 1				Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
$^{\rm C}$	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1



	Present Flip-flop inputs									
	state			w = 1				Output		
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
$^{\rm C}$	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

$$\begin{array}{c|cccc}
Q(t) \rightarrow Q(t+1) & J & K \\
\hline
0 \rightarrow 0 & 0 & d \\
0 \rightarrow 1 & 1 & d \\
\hline
1 \rightarrow 0 & d & 1 \\
1 \rightarrow 1 & d & 0
\end{array}$$

	Present	Flip-flop inputs								
	state	w = 0				w = 1				Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
$^{\rm C}$	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1



	Present	Flip-flop inputs								
	state	w = 0				w = 1				Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
$^{\mathrm{C}}$	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

$Q(t) \rightarrow Q(t+1)$	JK
0 → 0	0 d
$0 \rightarrow 1$	1 d
$1 \rightarrow 0$	d 1
1→1	d 0

And so on...

The Expression for z

	Present	Flip-flop inputs								
	state		w =	· 0		w = 1				Output
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	00 <mark>0</mark>	100	1d	0d	0d	110	1d	1d	0d	0
В	10 <mark>0</mark>	101	d0	0d	1d	110	d0	1d	0d	0
\mathbf{C}	10 <mark>1</mark>	101	d0	0d	d0	110	d0	1d	d1	1
D	11 <mark>0</mark>	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

z is equal to y₁

The Expression for J₃

	Present	Flip-flop inputs								
	state		w =	: 0			Output			
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
\mathbf{C}	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

The Expression for K₃

	Present	Flip-flop inputs								
	state		w =	: 0			Output			
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	100	101	d0	0d	1d	110	d0	1d	0d	0
\mathbf{C}	101	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	0d	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

K₃ is equal to 0

The Expression for J₂

9	Present	Flip-flop inputs									
	state		w =	: 0			Output				
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z	
A	000	100	1d	0 d	0d	110	1d	1 d	0d	0	
В	100	101	d0	0d	1d	110	d0	1 d	0d	0	
$^{\rm C}$	101	101	d0	0d	d0	110	d0	1 d	d1	1	
D	110	100	d0	d1	0d	111	d0	dO	1d	0	
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1	

The Expression for K₂

-	Present	Flip-flop inputs									
	state		w =	: 0			Output				
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z	
A	000	100	1d	0d	0d	110	1d	1d	0d	0	
В	100	101	d0	0d	1d	110	d0	1d	0d	0	
\mathbf{C}	101	101	d0	0d	d0	110	d0	1d	d1	1	
D	110	100	d0	d1	0d	111	d0	d0	1d	0	
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1	

The Expression for J₁

	Present	Present Flip-flop inputs								
	state		w =	= 0			Output			
	$y_3y_2y_1$	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z
A	000	100	1d	0d	0d	110	1d	1d	0d	0
В	10 <mark>0</mark>	101	d0	0d	1 d	110	d0	1d	0d	0
$^{\mathrm{C}}$	10 <mark>1</mark>	101	d0	0d	d0	110	d0	1d	d1	1
D	110	100	d0	d1	$\overline{0}d$	111	d0	d0	1d	0
Ε	111	100	d0	d1	d1	111	d0	d0	d0	1

$$J_1$$
 is equal to $w y_2 + \overline{w} y_3 \overline{y}_2$

The Expression for K₁

27	Present	Flip-flop inputs									
	state $y_3y_2y_1$		w =	: 0			Output				
		$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	$Y_3Y_2Y_1$	J_3K_3	J_2K_2	J_1K_1	z	
A	000	100	1d	0d	0d	110	1d	1d	0d	0	
\mathbf{B}	100	101	d0	0d	1d	110	d0	1d	$0\underline{d}$	0	
\mathbf{C}	101	101	d0	0d	d 0	110	d0	1d	d1	1	
D	1 <mark>10</mark>	100	d0	d1	0d	111	d0	d0	$1\overline{d}$	0	
Ε	1 <mark>1</mark> 1	100	d0	d1	d1	111	d0	d0	d0	1	

001

d

 K_1 is equal to $\overline{W} y_2 + W \overline{y_2} y_1$

All Logic Expressions

$$J_{1} = wy_{2} + \overline{w}y_{3}\overline{y}_{2}$$

$$K_{1} = \overline{w}y_{2} + wy_{1}\overline{y}_{2}$$

$$J_{2} = w$$

$$K_{2} = \overline{w}$$

$$J_{3} = 1$$

$$K_{3} = 0$$

$$z = y_{1}$$

Questions?

THE END